

# TOPIC: Novel Low Df Thermosetting Film and Photo Imageable Film

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1 INTRODUCTION
2 FILM AND ITS PROPERTY
3 PERFORMANCE OF THE FILM
4 PHOTO TYPE INSULATION FILM
5 CONCLUSION

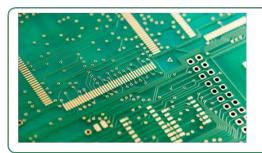
**QUESTION & ANSWER** 



## 1. INTRODUCTION

#### ~About TAIYO INK MFG

## **Taiyo Holdings Corporate Overview & Business Domain**



#### **Electronics Material**

- for Printed Circuit Board
- for IC Package Substrate
- for Flexible Circuit Board
- for Build-up Structure





Established: Sep.29 1953

Capital: 9.8B JPY

Employee: 2137



#### **Others**

- Energy
- Food
- IT Systems
- Other fine Chemicals Business



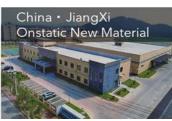
## 1. INTRODUCTION

#### ~About TAIYO INK MFG

## Electronics Business Group Network











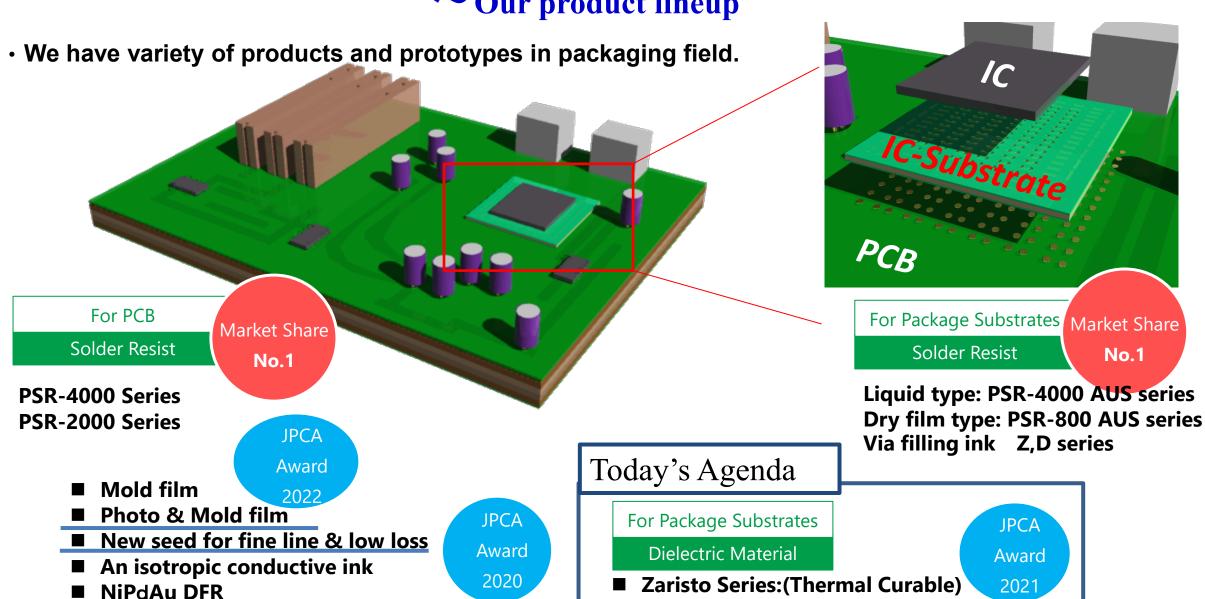






## IMAPS 19th Contellence on DEVICE PACKAGING | March 13-16, 2023 / Fountain Hills, AZ USA

## **∼**Our product lineup



**PVI Series:(Photo imageable)** 



## 1. INTRODUCTION

## ~Background of this study

• There is increasing need to reduce transmission loss at mm wavelength.

Transmission loss  $(\alpha)$  = Conductor loss  $(\alpha_c)$  + Dielectric loss  $(\alpha_d)$ 

Conductor loss at unit length (ac)

$$\alpha_C = \frac{2.26 \times 10^8 \times \sqrt{f}}{W}$$

f: wavelength

W: Wiring width

 $\varepsilon_r$ : Dielectric constant

Tanδ: Dielectric loss

Dielectric loss at unit length (ad)

$$a_d = 90\sqrt{\varepsilon_r} \tan \delta \times f$$

Substrate film

Low Dk, Df for decrease dielectric loss

Cu adhesion on smooth surface for decreasing conductor loss



## IMAPS 19th Conference on DEVICE PACKAGING | March 19-16, 2020 | Fountain Hills, AZ USA

## **~**Our strategy

Existing low Dk/Df films: poor physical property and process ability, since these were thermoplastic resins.

		Target Value	Epoxy film (Low Df type)	LCP, PPE, PPS
Electrical	Dk (10GHz)	<3.2	× (3.4)	0
property	Df (10GHz)	<0.003	× (0.005)	0
	Tg (C, TMA)	>180	<b>x</b> (160)	O (x : PPS)
	Water absorption(%)	<0.1	<b>x</b> (0.4)	0
	HAST endurance (130C, 85%, 500hr)	0	0	-
Physical	Thermosetting	0	0	×
property	Low CTE (x-y axis)	<25ppm	O (18)	× (O: LCP)
	Low CTE (z axis)	<25ppm	O (18)	×
	E'less Cu adhesion	>4N/cm	O (4)	×
Process	Laser via	0	0	Δ
ability	desmear	0	0	Δ
	E'less Cu condition	O 000418	0	×

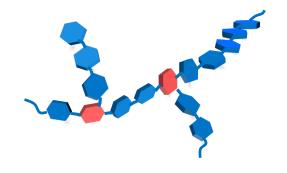
Convert thermoplastic PPE to thermosetting resin to improve the property.



## 2. FILM AND ITS PROPERTY

## ~ Film making process and electrical property

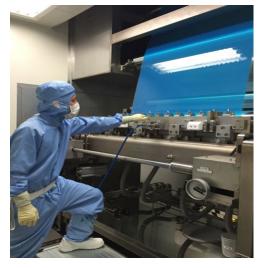
Synthesis of Novel Curable PPE



+ Formulating filler, initiator, crosslinking agents, and so on.

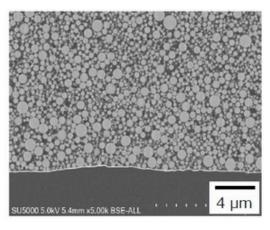


Film made by coating process



	Developed film
Dk (10 GHz)	O 3.1
Df (10 GHz)	O 0.0016
T <sub>g</sub> (°C)TMA	O 195
CTE (xy, ppm)	O 18
CTE (z, ppm)	O 18
Water absorption (%)	O 0.04





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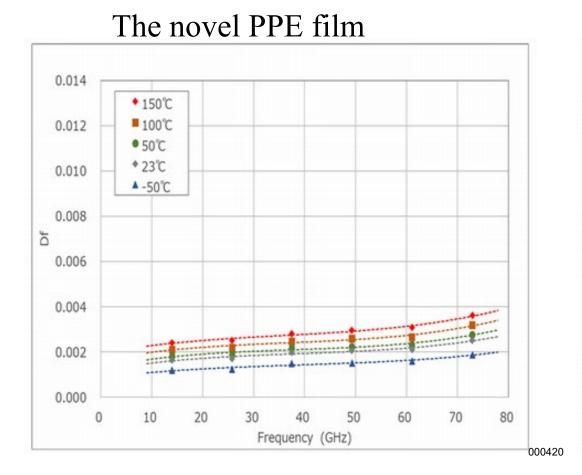


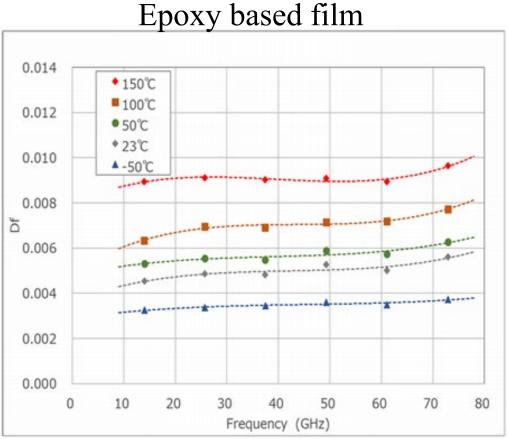
## 2. FILM AND ITS PROPERTY

## **∼** Df: temperature dependence

The novel PPE film showed lower increase of Df compared to that of epoxy film.

= Transmission loss would not increase with temperature.



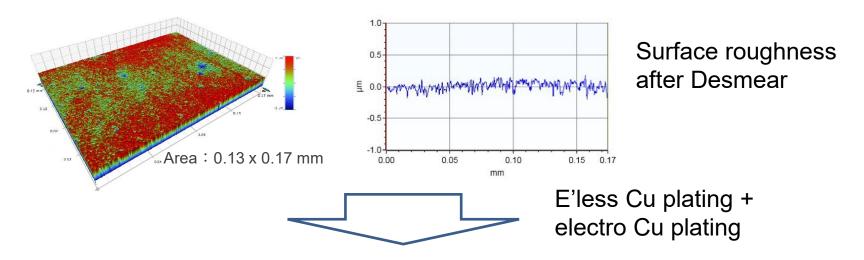




## 2. FILM AND ITS PROPERTY

#### ~ E'less Cu adhesion

The novel PPE film showed good adhesion to E'less Cu even on the smooth surface Ra=ca.60nm.



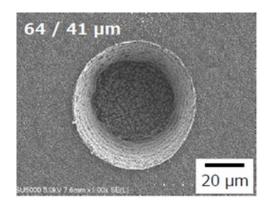
		Criteria	The film
Surface roughness	After desmear process	<100nm	Ra=ca.50nm Pass
Peel strength E' less Cu	Initial	>3.0N/cm	4.5N/cm Pass
	85deg.C85% After 100hrs	>3.0N/cm	4.2N/cm Pass



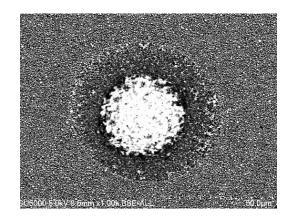
## 2. FILMAND ITS PROPERTY

## **∼** Laser via process ability

The novel PPE film showed similar laser via process ability to that of the existing epoxy film.



CO<sub>2</sub> laser via



Smear was depleted after ordinal desmear process

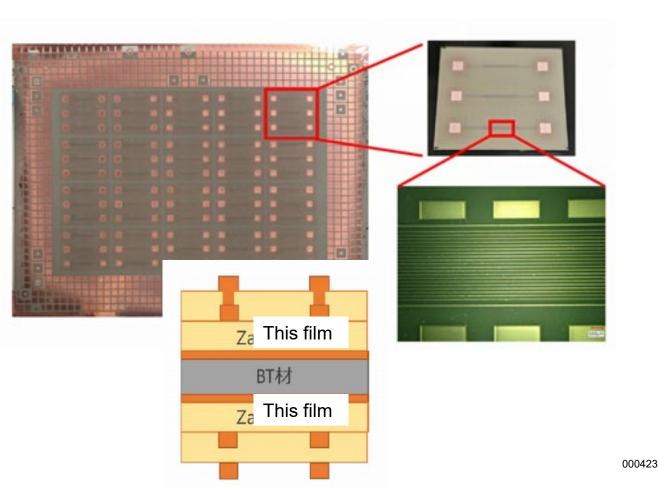
Chemical: Atotech S.60°C 5min K:80°C 20min R:40°C 5min

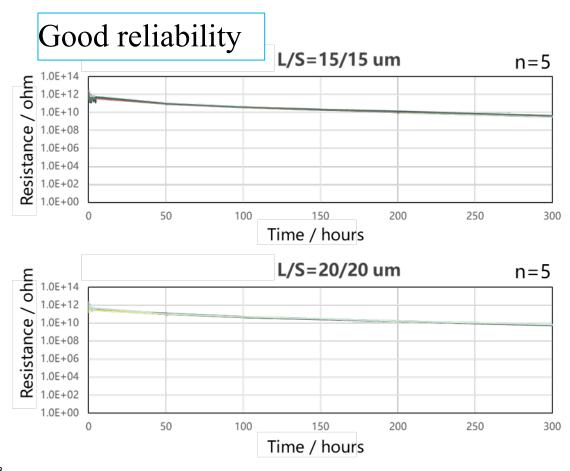
	Excimer laser				
	Total	Target:10umΦ	Target:5umΦ		
Picture  Top (umΦ)	50μm 20μm 100μm 10μm 5μm	<u></u>			
Bottom (umΦ)		9	4		
Ratio( %)		82	67		



## ~ Making stacked substrate and reliability

We have made stacked substrate using the novel PPE film, with comb electrode L/S=15/15μm, 20/20μm by SAP process, and measured B-HAST (135°C, 85RH, DC5V).

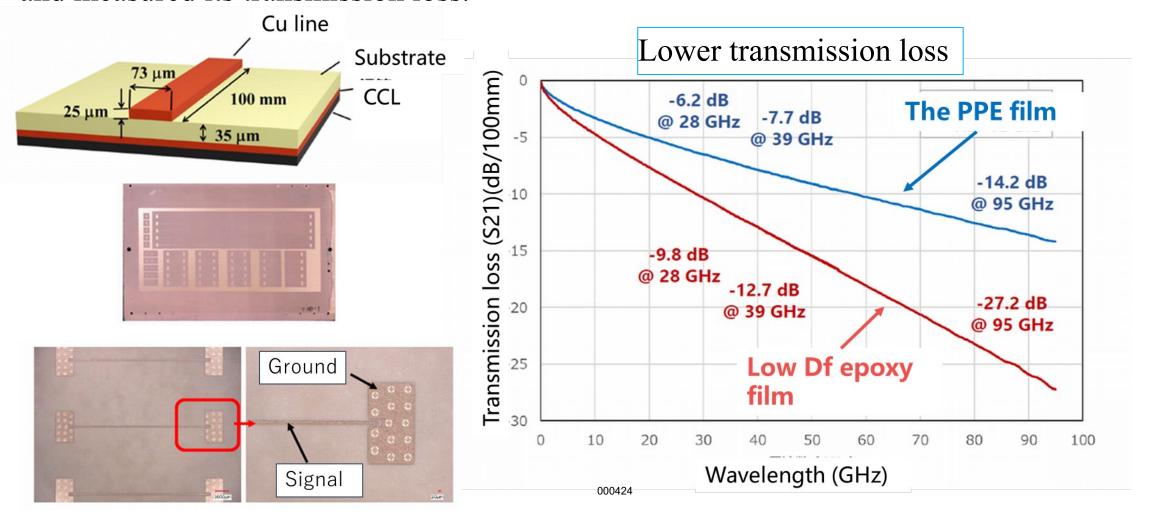






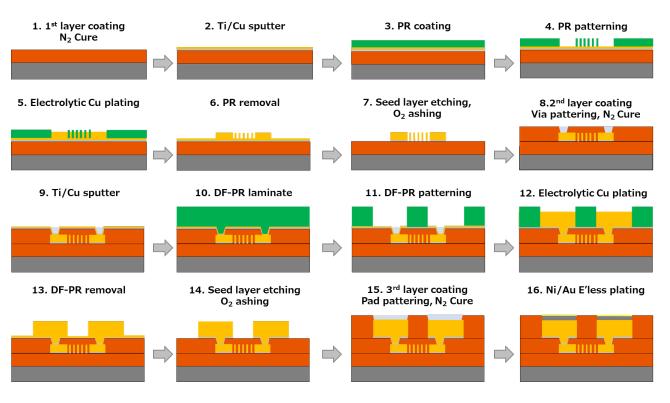
## **∼** Making stacked substrate and reliability

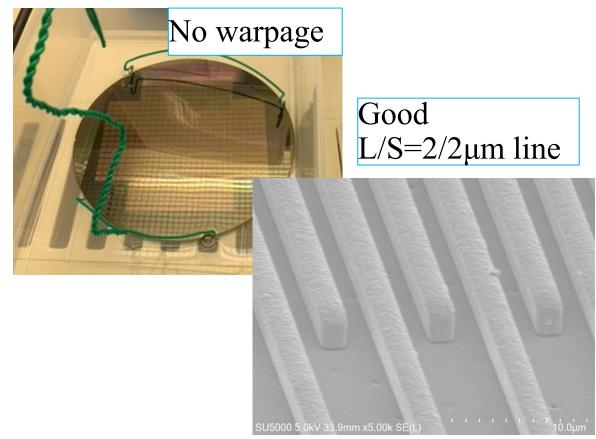
We have made strip line substrate using the novel PPE film and the epoxy film, and measured its transmission loss.



#### **∼**Making RDL of the chiplet

We have made 3 layers RDL stacked on Si wafer, with  $L/S=2/2\mu m$ .







#### ~Conclusion

The novel PPE film would be a good candidate for lower transmission loss substrate.

		Target Value	Epoxy film (Low Df)	PPE	Developed PPE film
Electrical	Dk (10GHz)	<3.2	× (3.4)	O (2.6)	O (3.1)
property	Df (10GHz)	<0.003	× (0.005)	O (0.0025)	O (0.0022)
	Tg (°C, TMA)	>180	× (160)	O (195)	O (195)
	Water absorption(%)	<0.1	× (0.4)	O (0.03)	O (0.04)
	HAST endurance (130°C, 85%, 500hr)	0	0	_	0
Physical	Thermosetting	0	0	×	0
property	Low CTE (x-y axis)	<25ppm	O (18)	×	O (18)
	Low CTE (z axis)	<25ppm	O (18)	×	O (18)
	E'less Cu adhesion	>4N/cm	O (4)	×	O (4)
Process	Laser via	0	0	×	0
ability	desmear	0	0	Δ	0
	E'less Cu condition	O 000426	0	×	0



## ~ Background

Normally, photo type liquid Poly Imide was used as RDL layer. It had its disadvantages, but there were no promising candidates in existing materials.

		Target Value	Epoxy film (conventional)	Photo PI (conventional)	Photo Solder Resist film (conventional)
Electrical	Dk (10GHz)	<3.4	0	0	0
property	Df (10GHz)	<0.02	0	0	0
Physical	Tg (°C, TMA)	>150	0	0	0
property	Low CTE	<25ppm	0	×(ca.50-60)	×(ca.40-50)
Process	Curing temperature	>180°C	0	×(ca.250)	0
ability	Lamination stacking	OK	0	×(Wet process)	0
	Via formation	Photo process	× (Laser)	0	0
	desmear	No	× (Required)	0	0
	E'less Cu condition	Wet	0	0	×
		Dry (sputter)	000427	0	0

#### ~ Criteria

There would be below 4 criteria for promising RDL material.

- 1: Film type: For simple process ensuring surface flatness.
- 2: Photo imageable: For simple process ensuring via formation.
- 3: Electroless cupper plating: For simple process ensuring wire formation.
- 4: Low CTE and low curing temperature: For avoiding warpage.



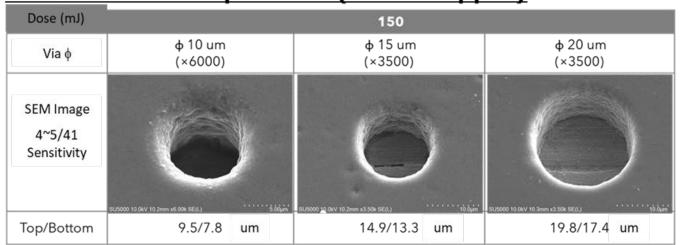
## **∼** Developed film

We had developed photo type insulation film for RDL. It had passed the 4 criteria for promising RDL material.

#### **Material properties**

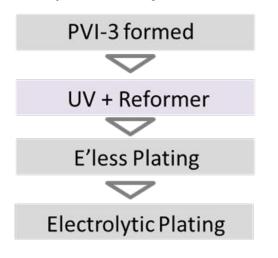
	Dk	Df	CTE	Tg (°C)	Tg (°C)
	(10GHz)	(10GHz)	(a1)	TMA	DMA
PVI-3	3.4	0.015	25ppm	150	150

#### Micro-via with 15µmt PID (i-line stepper)



#### Cu plating adhesion

Uyemura desmear-free new process (Adhesion promoter)



	Peel strength
E'less Cu	4.5 N/cm



## **∼** Developed film

#### The film showed good B-HAST endurance.

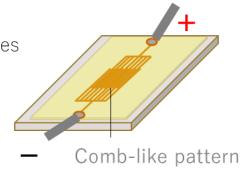
#### BHAST between lines

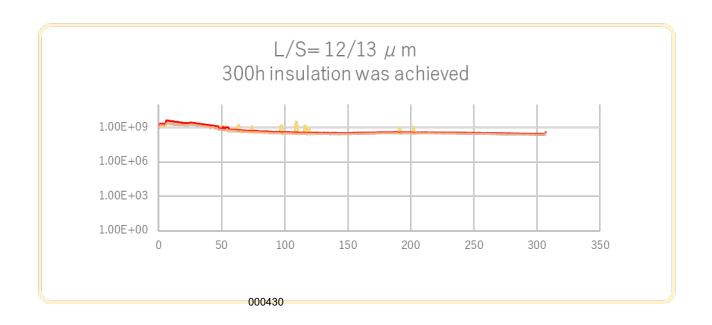
Preconditioning :125 deg.C/24 h  $\Rightarrow$  85 deg.C/60%/168 h  $\Rightarrow$  Reflow 260 deg.C/3 times

B-HAST: 130deg.C,85% / 5 V (Failure criteria:1.0E+6 ohm)

Chamber: HASTEST® MODEL PC-R8D (Hirayama)

Measurement: MIGRATION TESTER MODEL MIG-8600B (IMV)





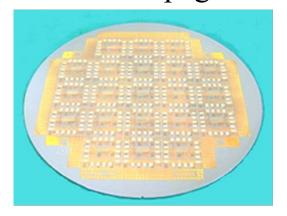


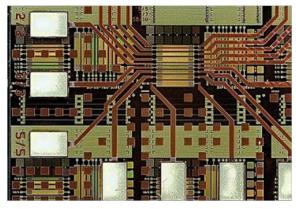
## IMAPS 19th Conference on DEVICE PACKAGING | March 13-16, 2023 | Fountain Hills, AZ USA 4. PHOTO TYPE INSULATION FILM

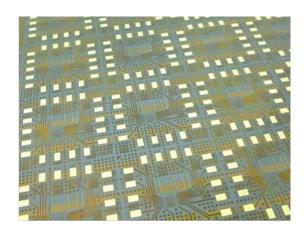
## **∼** Developed film

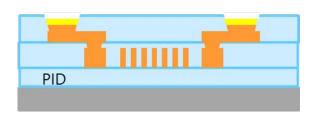
We had made below 3 layer RDL on 8 inch Si wafer.

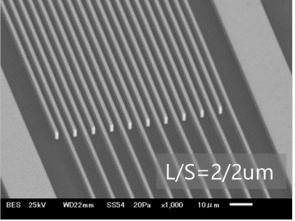
There observed no warpage.







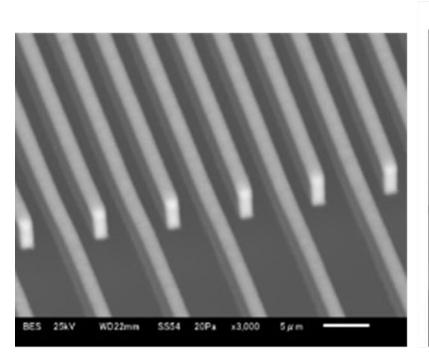




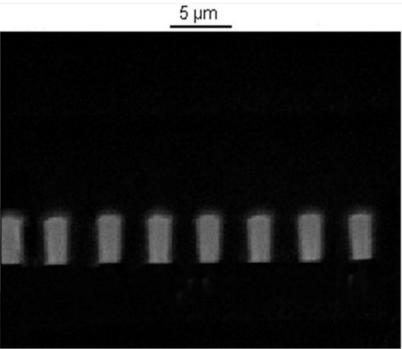


## **∼** Developed film

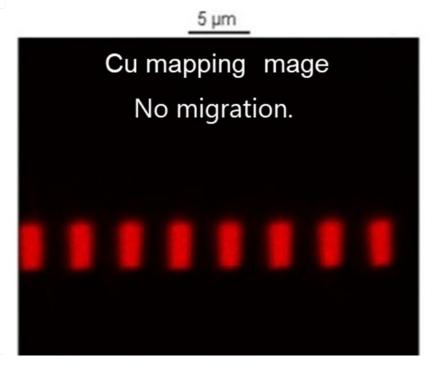
The film showed good embeddability, and good B-HAST endurance.



Initial: Cross section



After B-HAST 300hr



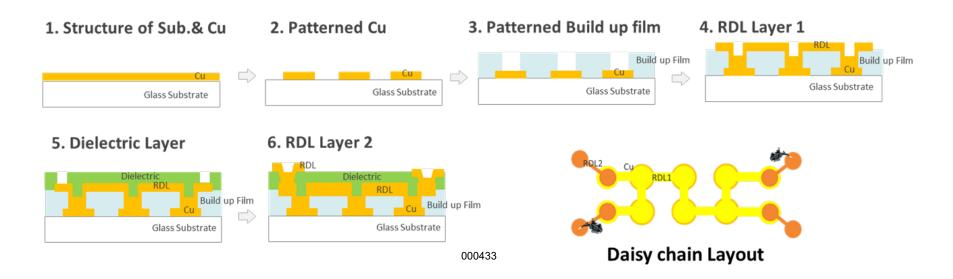


#### ~ Brief introduction

ITRI had presented new RDL formation process by using our photo type insulation film at SEMICON TAIWAN 2022.

# High resolution & Low Temp. process RDL Structure by Using Digital Lithography for IC substrate Applications

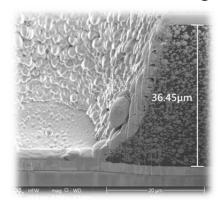
□ ITRI has developed a redistribution layer (RDL) structure by using digital lithography & low temp. sputtering technologies. And it achieves in a high resolution & low Temp. process structure that can be used as a good solution to minimize the thermal budget in IC substrate production.

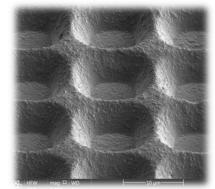


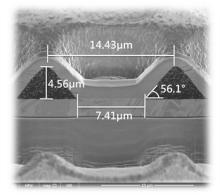
#### ~ Brief introduction

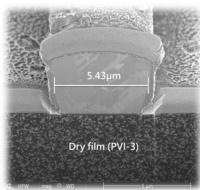
## High resolution & Low Temp. process RDL Structure by Using Digital Lithography for IC substrate Applications

□ ITRI has developed a redistribution layer (RDL) structure by using digital lithography & low temp. sputtering technologies. And it achieves in a high resolution & low Temp. process structure that can be used as a good solution to minimize the thermal budget in IC substrate production.









X'view of Build up Film (TAIYO)

Via size of PVI-3 by DLT

X'view of Trace on PVI-3

Items	This Tech.	Competitor Dry film	
PID Type	Dry film		
Max. Process Temp.	180 °C	250°C	
PID Resolution	7 μm	10 μm	

PID: Photo Image Dielectric

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## 5. CONCLUSION

- 1: We have synthesized "the novel thermosetting PPE resin" and made low Dk/Df film by formulating the resin and other compounds.
- 2: The film showed lower Df compared to that of the existing epoxy film, especially at high temperature.
- 3: The film showed good physical property and process ability. The stacked substrate of the film showed good reliability, and strip line on the film showed lower transmission loss compared to that of the existing epoxy film.
  - The film would be a good candidate for substrate film of high wavelength.
- 4: We have developed the photo type insulation film, which could be used for RDL and substrate.



Logo of Affiliation