

Hybrid Bonding Based on Polyimide for Low Temperature Process

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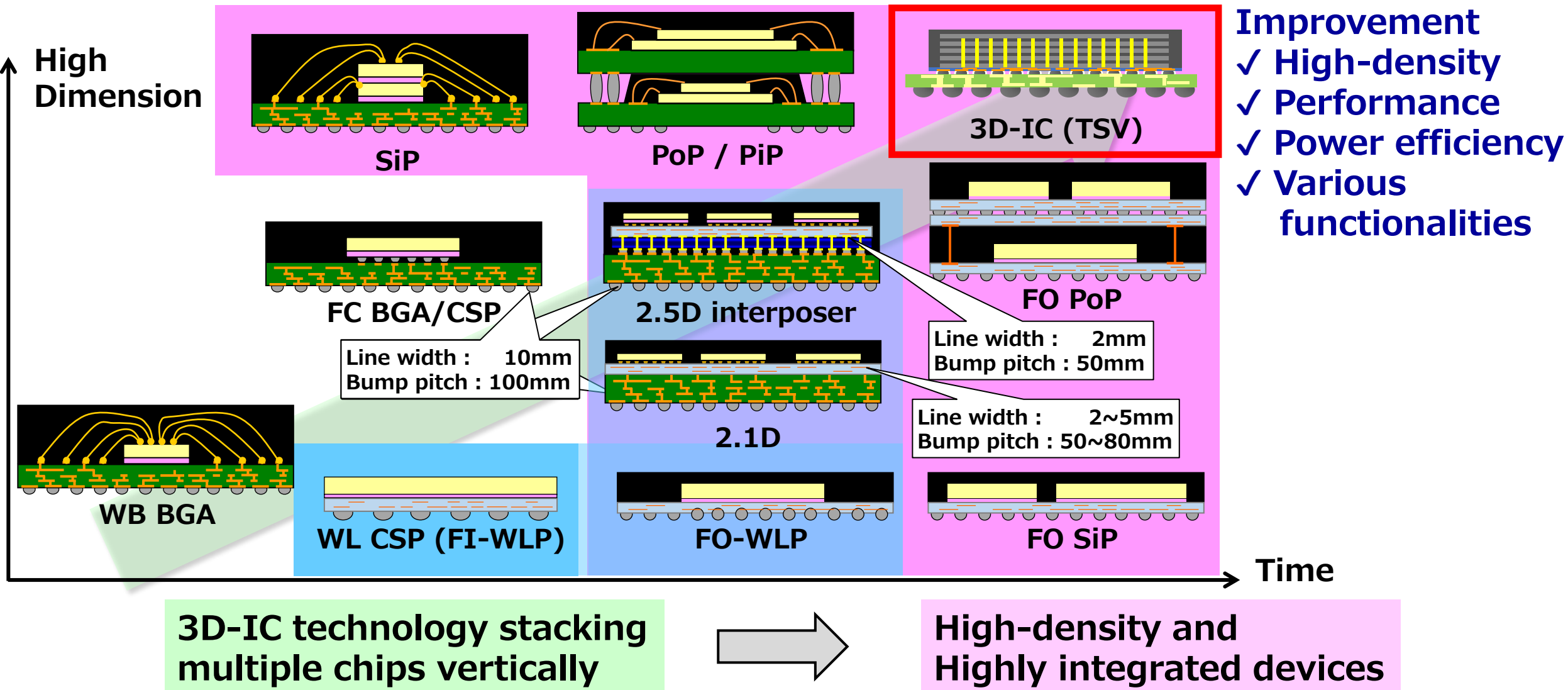
Outline

- 1. Introduction and Background**
- 2. Polymer Bonding Test**
- 3. Polyimide-to-Polyimide (PI-PI) Bonding Mechanism**
- 4. Hybrid Bonding Demonstration**
- 5. Summary**

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Semiconductor Chip Packaging Technology



1. Introduction and Background

Hybrid Bonding Technology (=HB)

Hybrid Bonding(HB) Technology



Hybrid Bonding is bumpless interconnect technology.

Interconnect	μ -bump	HB
Bump Density	1.0X	16.0X
Speed	1.0X	11.9X
Bandwidth	1.0X	191.0X
Power Efficiency	1.0X	0.05X

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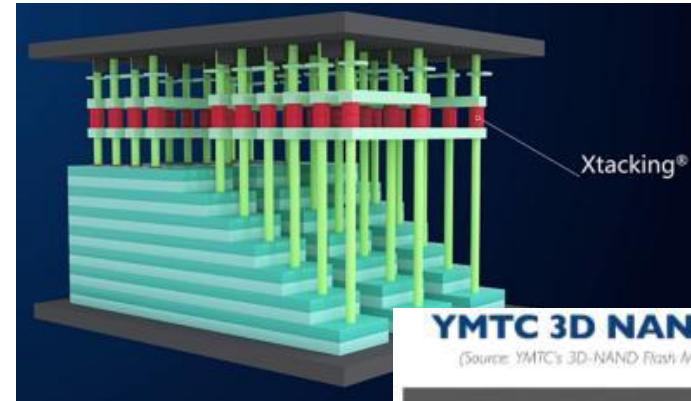
Sony : CMOS-Image Sensor



Source : Sony

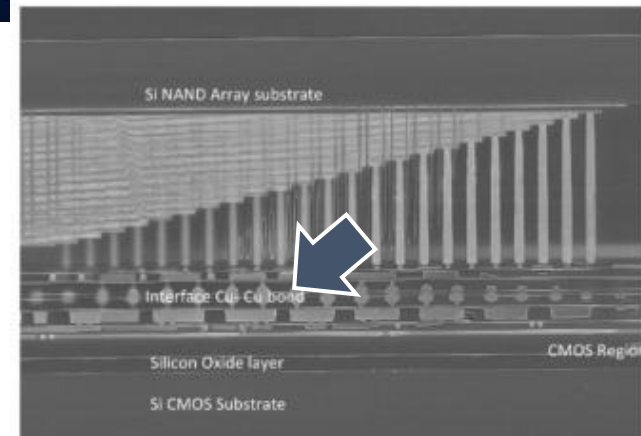
Wafer to Wafer (W2W) process has already POR Dielectrics = SiO₂ (Inorganic)

YMTC : NAND Memory



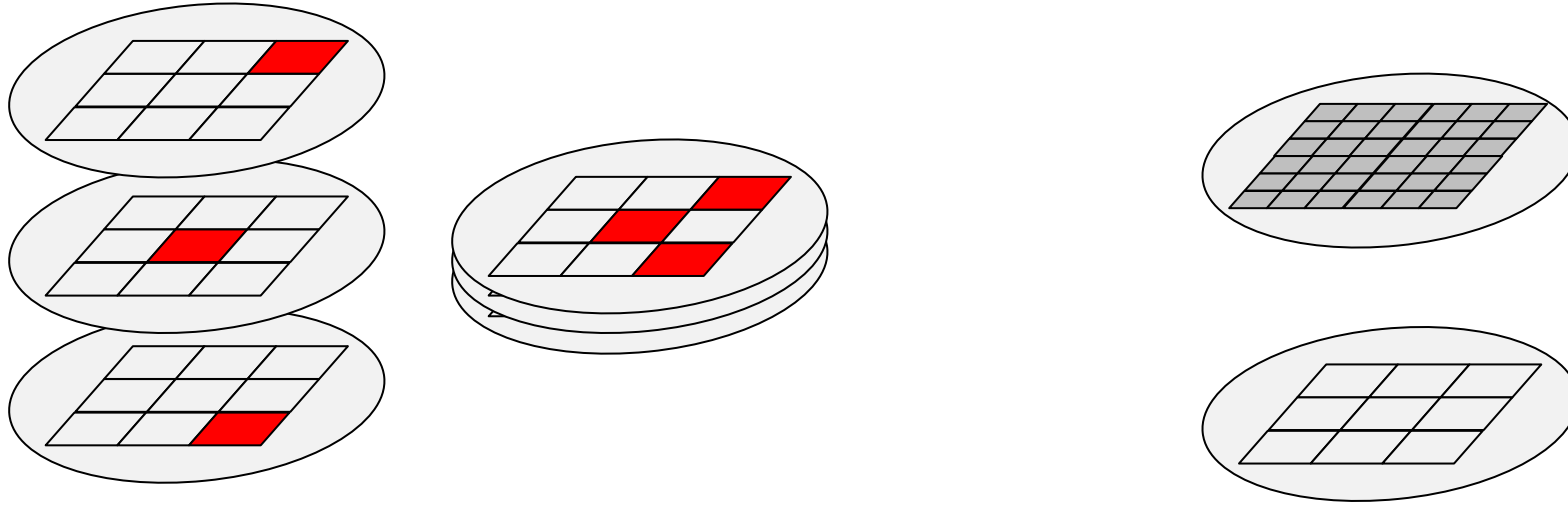
Source : YMTC

YMTC 3D NAND die cross section
(Source: YMTC's 3D-NAND Flash Memory report, System Plus Consulting, 2020)



Issue of the Wafer to Wafer bonding

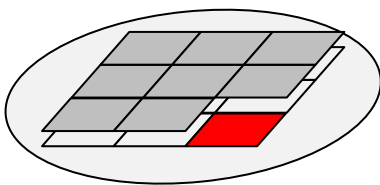
W2W



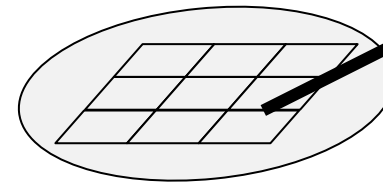
The more stacking wafers,
the more defects

Cannot bond difference size chip

Chip to Wafer(C2W)



Bonding with
only known good die



Various size chip
available

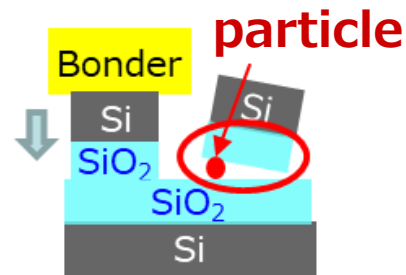
Benefits of C2W HB : Free combination of design with high yield.

Issues for C2W Inorganic HB

Issue

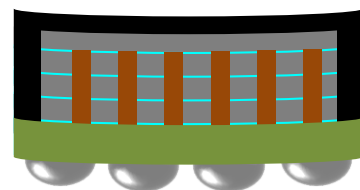
Voids problem due to particle from dicing

➔ Bonding failure and Defective insulation



Warpage due to multiple stacking of thinned chips

➔ Crack, mis-alignment and bonding failure



Low yield **Low reliability**

Solution

Inorganic Hybrid Bonding



Polymer Hybrid Bonding

Low temperature processable (<250°C)

Polymer dielectric materials

Wide range CTE control (5~100ppm/K)

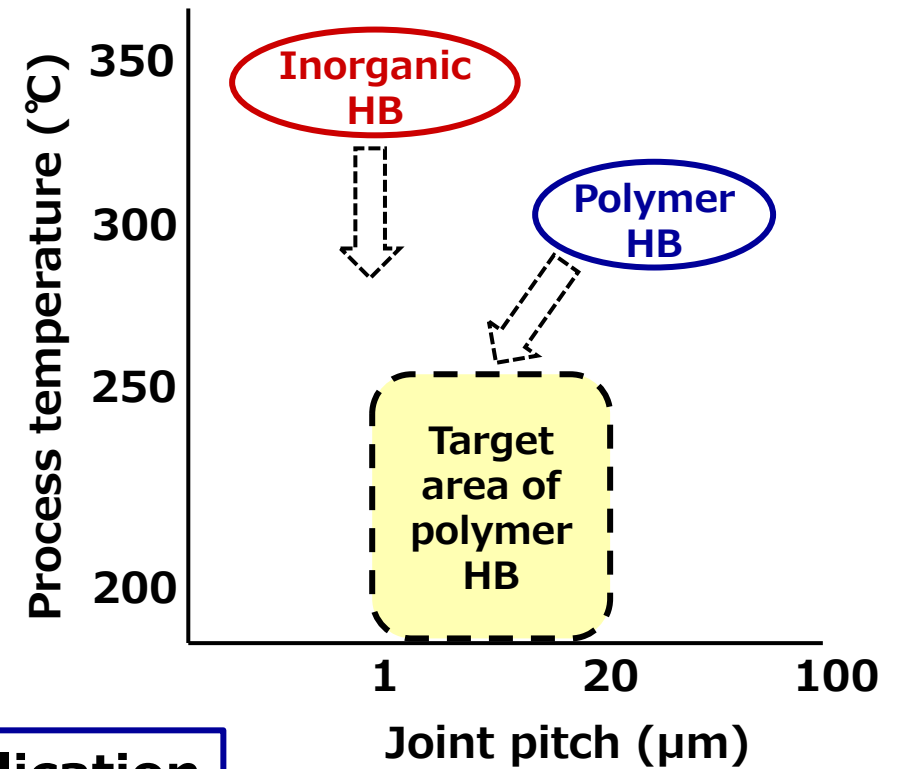
Low modulus (<5GPa)

Polymer hybrid bonding could be the solution for C2W HB issues.

Process Capability and Target

	Inorganic HB (SiO ₂)	Polymer HB
Bump pitch	1μm	3~10μm
Max. Process temp.	≥300°C (Annealing)	250°C (Annealing)
Bonding yield *particle influence	Low	High
Thermal conductivity	High	Low
Application	Logic/SRAM	HBM die stack

Status / Development Target



Advantages

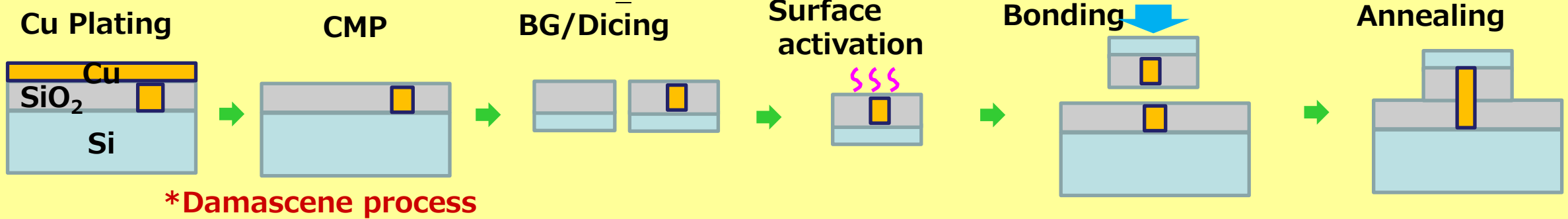
- SiO₂ : Fine pitch and thermal conductivity
- Polymer : Low temp. process with higher yield

Application

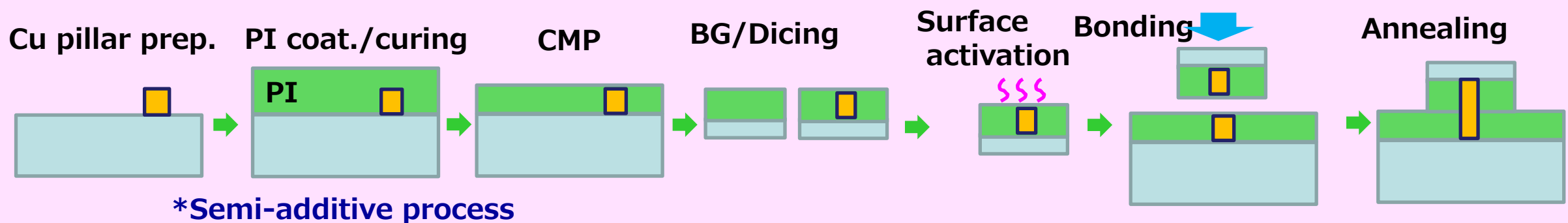
- Logic / SRAM with ≤1μm pitch
- HBM die stack with 1~20μm pitch

Process of Inorganic and Polymer HB

1. Inorganic Hybrid Bonding (SiO_2)



2. Polymer Hybrid Bonding



Establishing polymer CMP process could be a challenge.

Mechanism of polymer bonding with surface activation is still at an immature stage.

Polymer HB Challenges

Low Temperature & Low Pressure Bonding

Investigation about **criteria of bonding process conditions.**

*Optimization for each polymers

Bonding Mechanism

Estimation of the **polymer-to-polymer bonding mechanism.**

*Analysis of surface after plasma activation

Polymer CMP Process

Control of **Cu topology and Cu height** after CMP process.

*Optimization of CMP conditions

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Material Properties and Design for HB

✓ Low temp. curable feature

	Target	Polyimide-A (PI-A)	Polyimide-B (PI-B)
Curing condition	$\leq 250^{\circ}\text{C}$	$230^{\circ}\text{C} \times 60\text{min}$	$200^{\circ}\text{C} \times 60\text{min}$
T _g [°C]	$> 200^{\circ}\text{C}$	150, 280	283
Td ₅ [°C]	High	350	440
CTE [ppm/K]	Low	55	23
Elongation [%]	–	100	62
Modulus [GPa]	–	1.7	3.2
Design	–	High elongation/toughness & Low modulus type	Low CTE type *close to Cu (=18ppm/K)

***Suppression of stress and warpage**

PI-A (Low modulus) and PI-B (Low CTE) were designed for polymer HB study.

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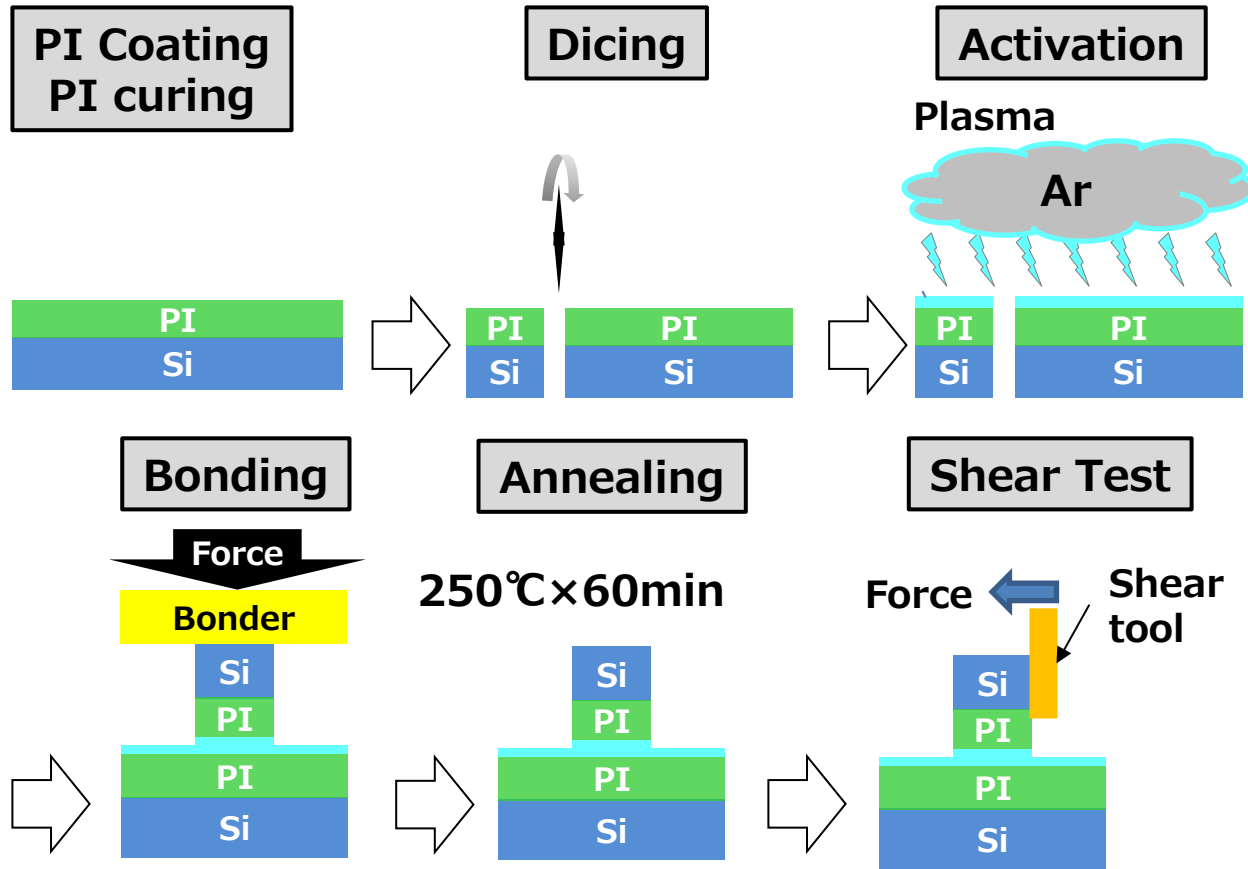
Polymer CMP Process

Control of Cu topology and Cu height after CMP process.

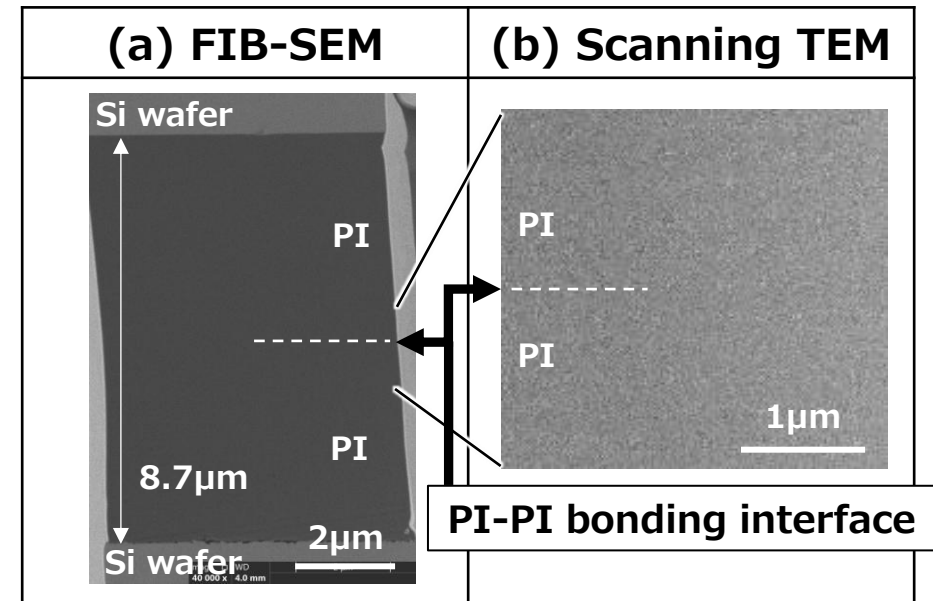
*Optimization of CMP conditions

2. Polymer Bonding Test

Test Method of General PI-PI Bonding



*Cross-section image of PI-PI bonded interface (Die shear strength >25MPa)

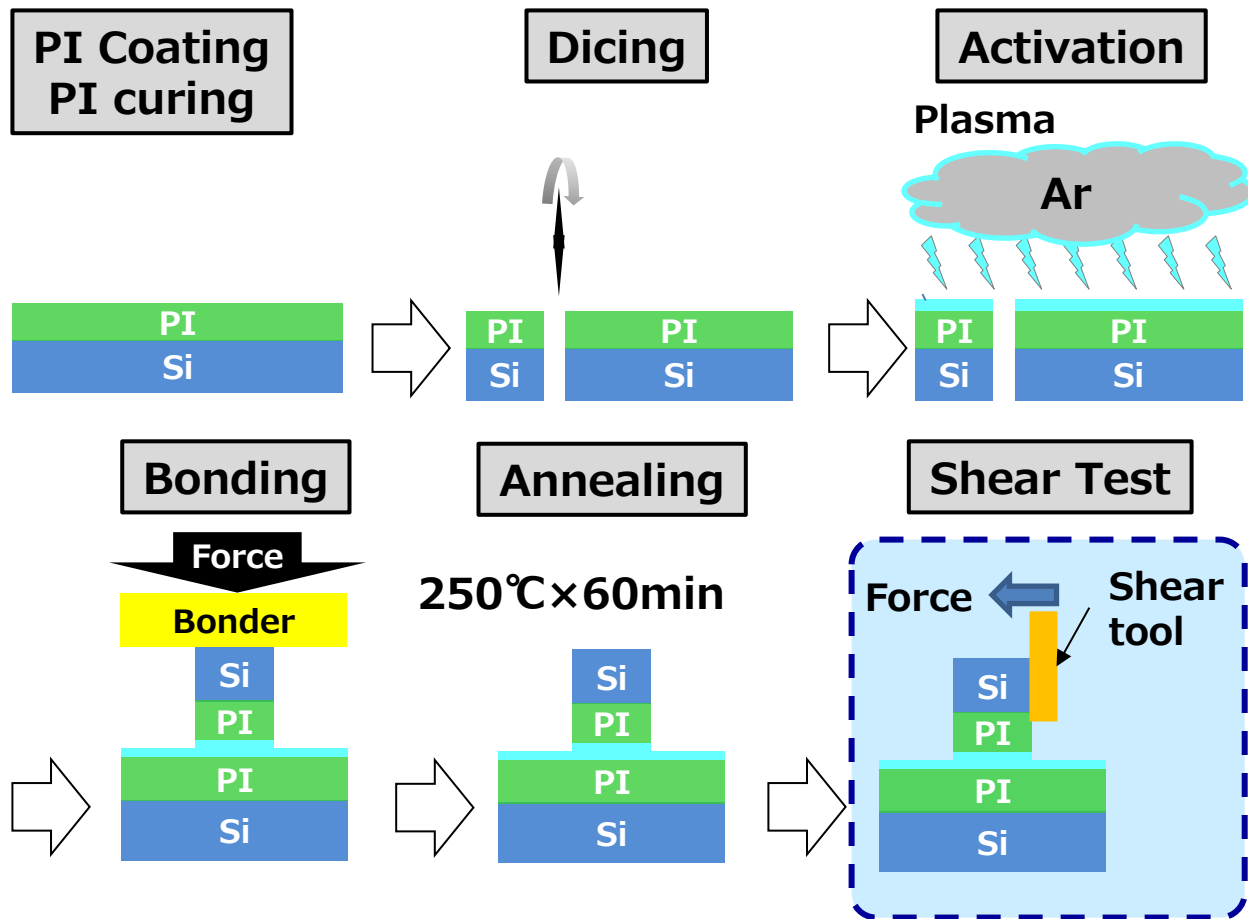


✓ No voids in submicron-scale

PI-PI bonding strength was evaluated by die shear test
Die shear strength of >25MPa was sufficient (*FIB-SEM, STEM)

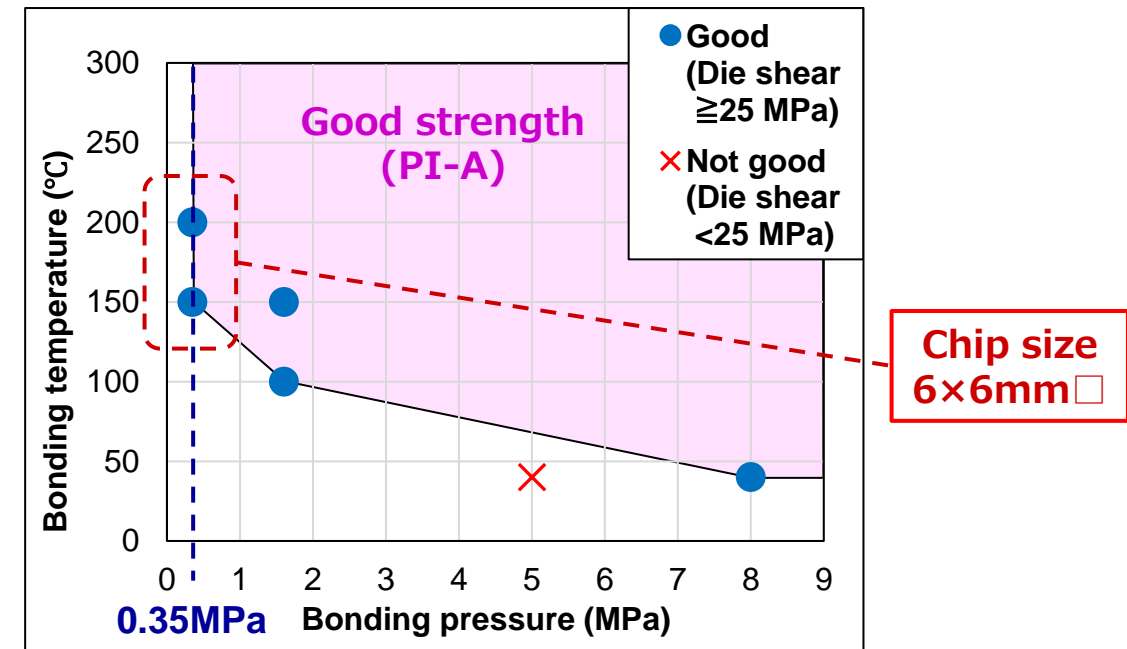
2. Polymer Bonding Test

Result of PI-PI Bonding Test (PI-A)



PI-A *Low modulus

After annealing

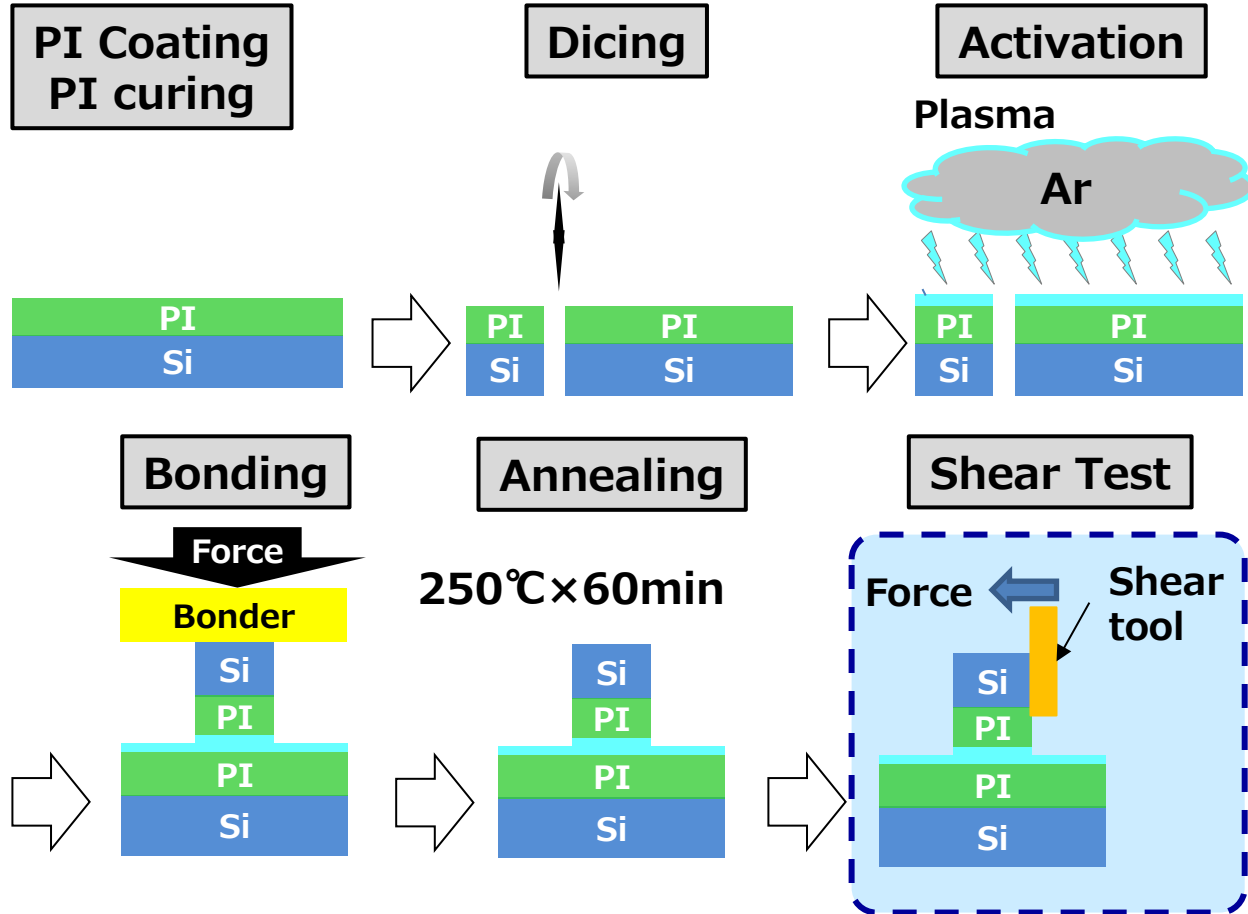


*Plasma condition : 20Pa, 20sccm, 20s, RF power=150W
*Chip size : 2.5×2.5mm □ (except for 0.35MPa)

**PI-A : Low pressure & Low temp. bonding property.
Trade-off relationship between temp. and pressure.**

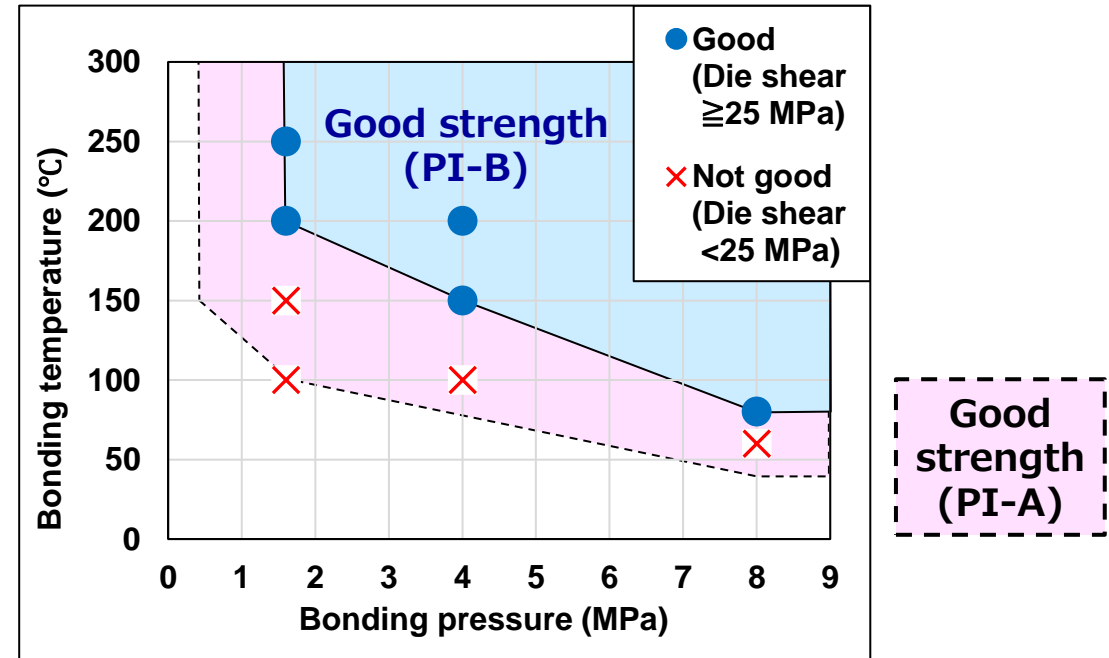
2. Polymer Bonding Test

Result of PI-PI Bonding Test (PI-B)



PI-B *Low CTE

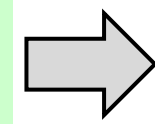
After annealing



*Plasma condition : 20Pa, 20sccm, 20s, RF power=150W

*Chip size : 2.5×2.5mm

PI-B : Relatively high pressure & high temp. bonding.
PI-A/PI-B : Different criteria of bonding process.



Mechanical properties were contributed

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3. Polyimide-to-Polyimide Bonding Mechanisms

Effect of Plasma Activation

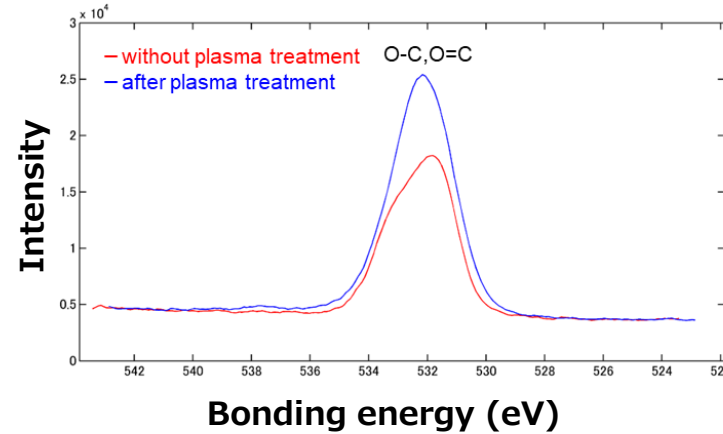
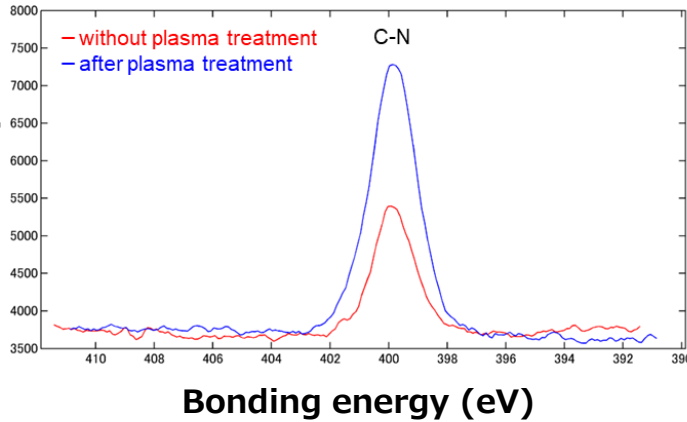
*Plasma condition :
20Pa, 20sccm, 20s, RF power=150W

■ X-ray Photoelectron Spectroscopy Analysis (XPS)

■ Surface Free Energy Analysis (SFE)

(a) N1s

(b) O1s



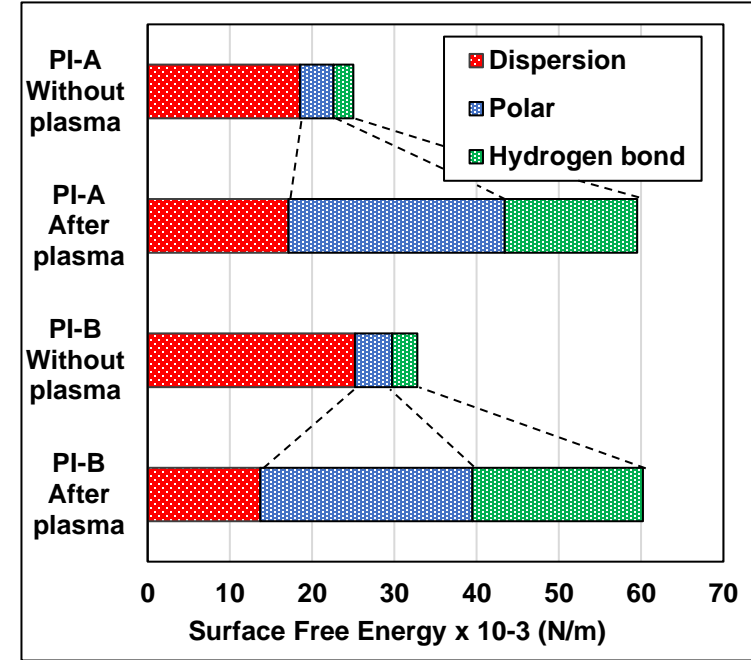
PI-B
*Low CTE

Plasma	C	N	O	-COOH
Before	1.00	0.03	0.29	0.001
After	1.00	0.09	0.49	0.013

- ✓ COOH group : Increased
- ✓ C-N bonding : Increased

PI-A
*Low modulus

PI-B
*Low CTE

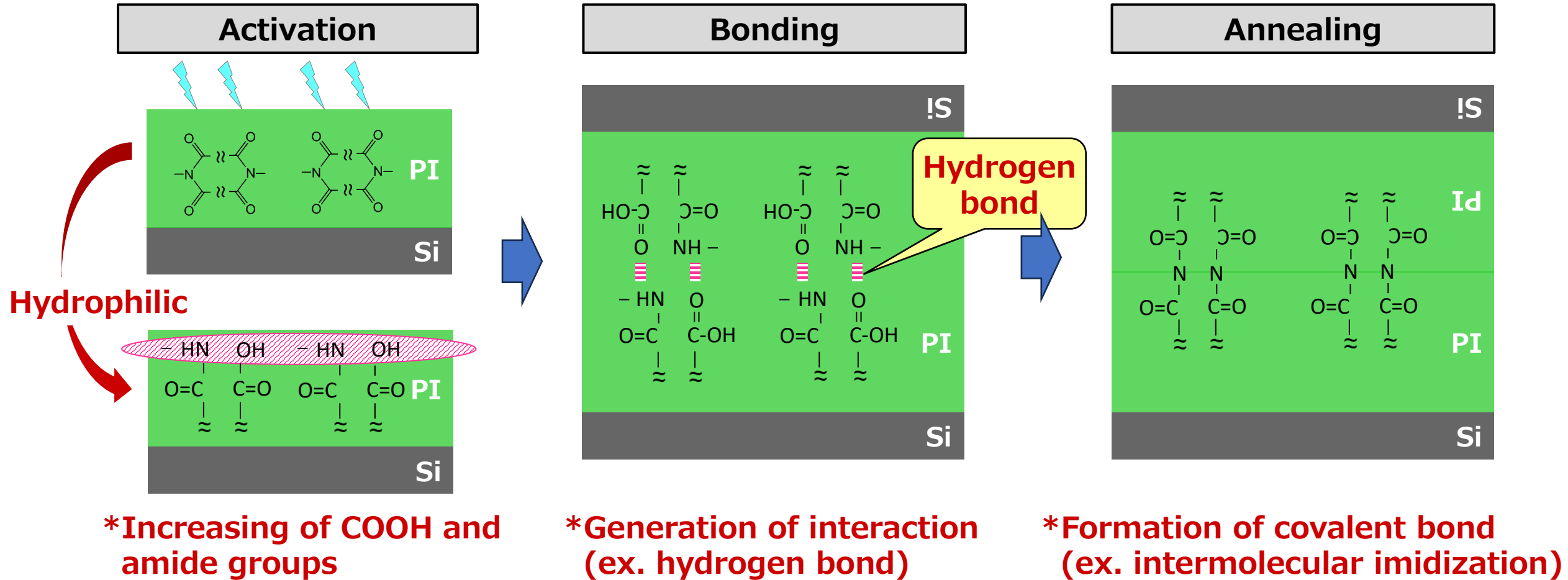


- ✓ Polar and hydrogen bond : Increased
- ✓ PI-A/PI-B : No significant difference

Plasma activation : Increasing of polar and hydrogen bond component

3. Polyimide-to-Polyimide Bonding Mechanisms

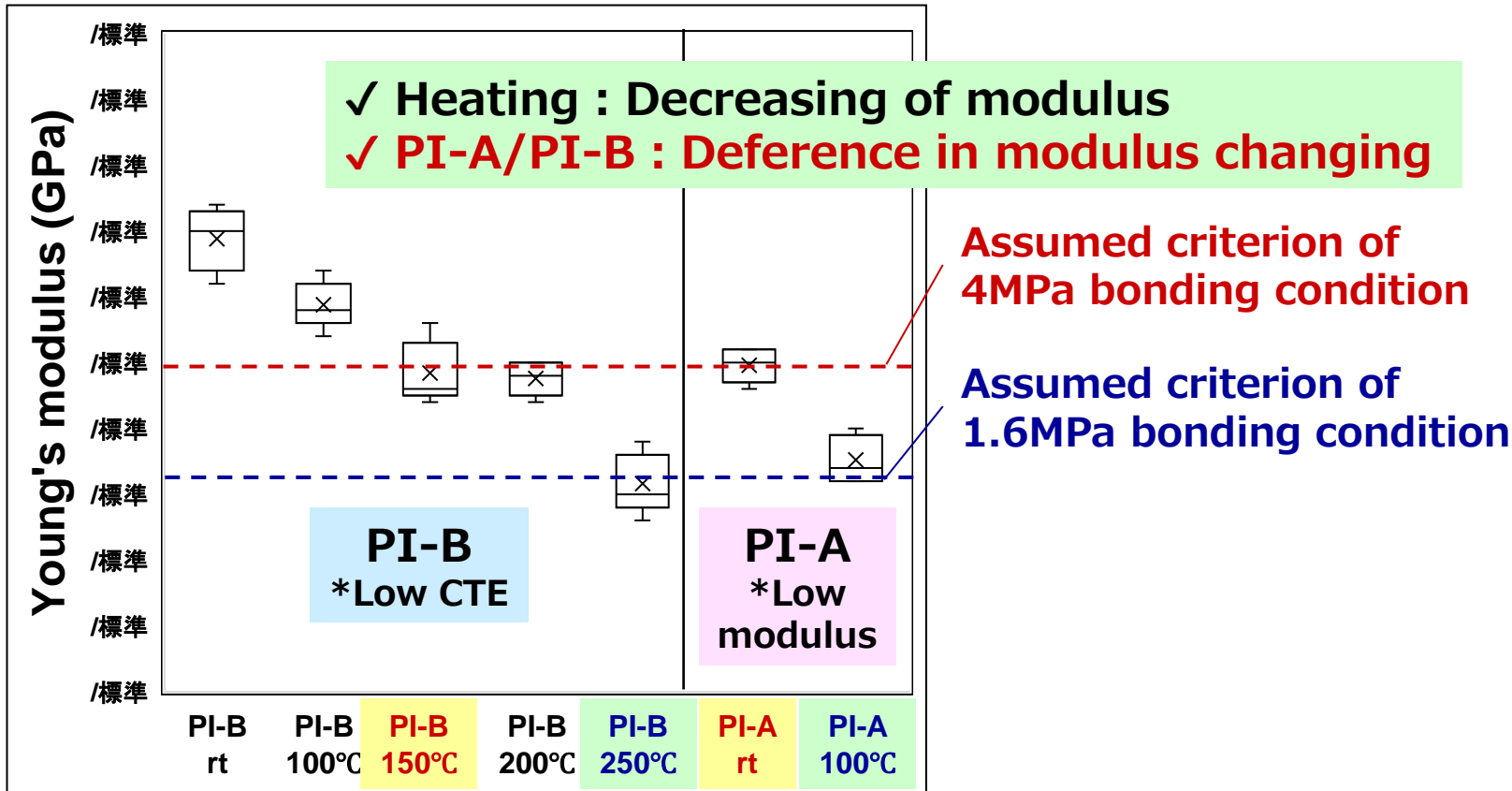
Assumed Bonding Mechanism



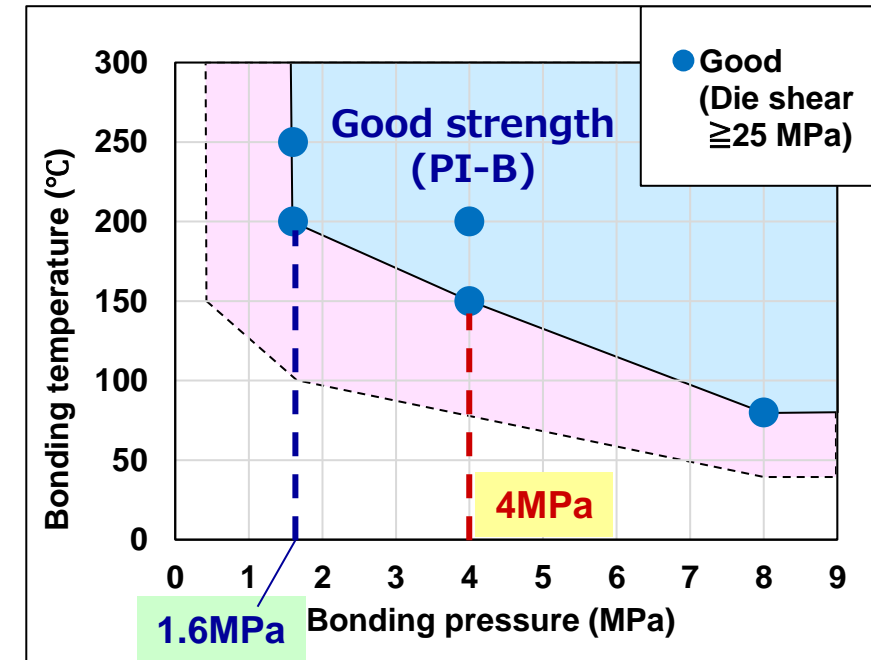
Bonding driving force :
 ① Interaction by hydrogen bond ② Covalent bond by imidization

Influence of mechanical Properties

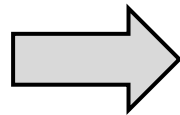
Young's Modulus vs. Temperature (Nano-indentation)



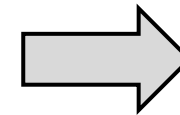
(After annealing)



Heating at specific temp.



Leading towards to certain modulus



Provide PI-PI bonding with good strength

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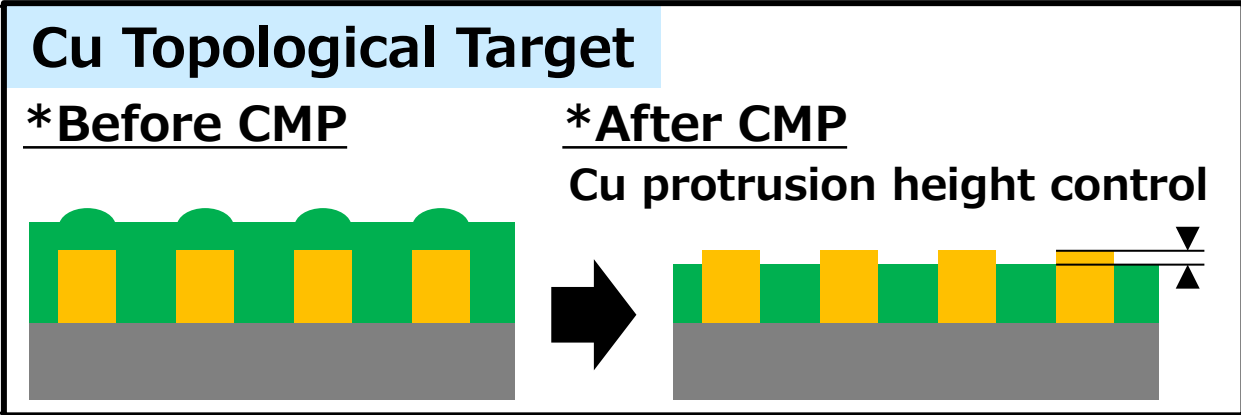
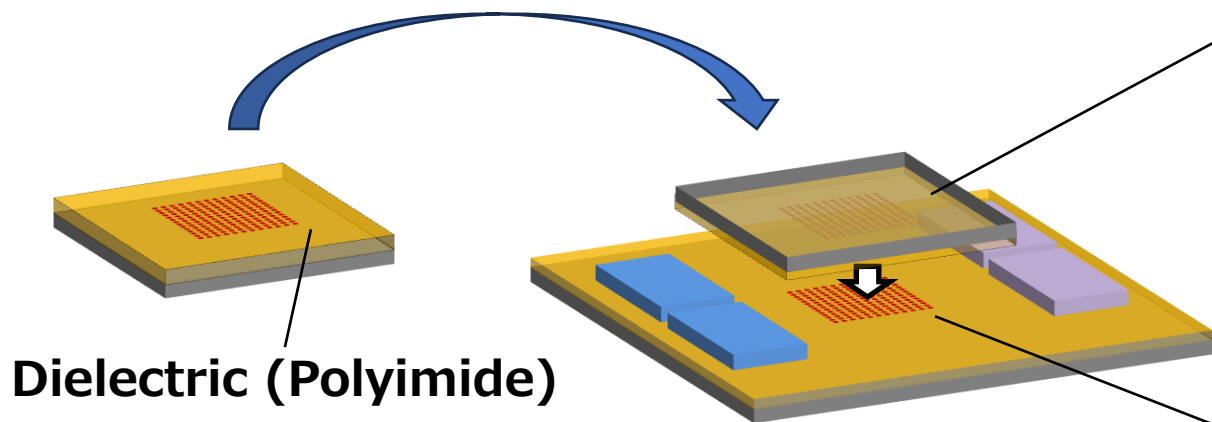
Polymer CMP Process

Control of **Cu topology** and **Cu height** after CMP process.

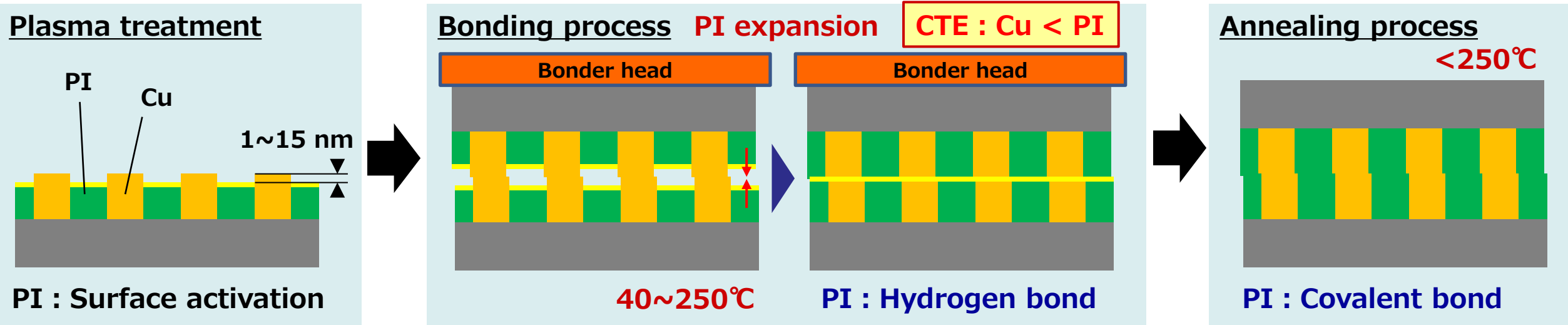
*Optimization of CMP conditions

4. Hybrid Bonding Demonstration

Control of Cu Topology for HB



Bonding Scheme (Current Target)



Toray's Target : Suitable Cu protrusion for HB is PI dependent (*CTE : Cu < PI).

Conditions of HB Test

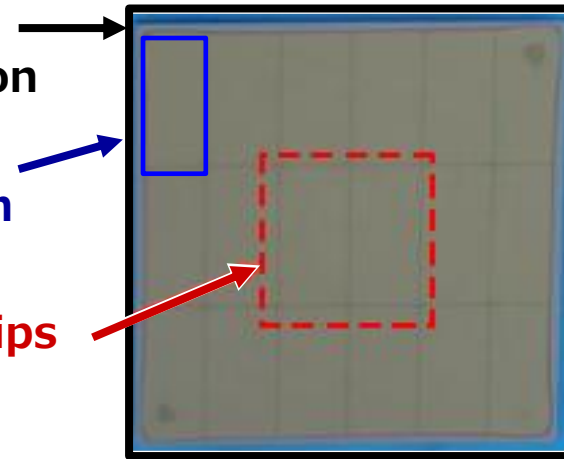
■ Coupon Sample Detail

Coupon size : 36mm×36mm
 18chips in a coupon

Small chip
 6mm×13mm

central small 2chips

Cu pillar : 5 μ m Φ , 10 μ m pitch



*Courtesy of Institute of Microelectronics,
 Singapore's Agency for Science

■ CMP conditions

Equipment :
 NF-300 (NANO FACTOR Inc.)
 Slurry :
 Alkaline type slurry
 Pad :
 Hard type pad
 Table speed :
 120 rpm
 Pad temp. :
 <40°C

*Courtesy of D-process inc., Japan

<Test method>

Step 1 : PI coating and PI curing of **PI-A (*Low modulus)**

Step 2 : CMP treatment of PI surface and Cu surface

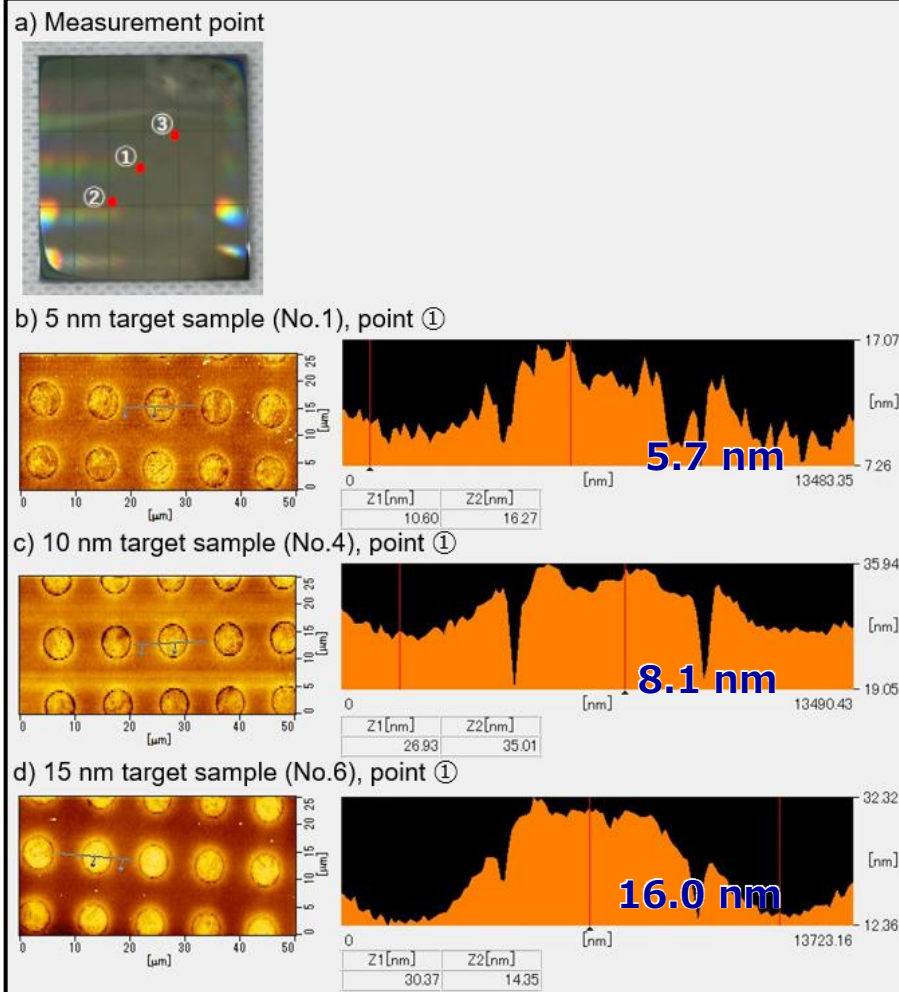
Step 3 : Measurement of surface profile of PI/Cu with central small 2chips

PI-A (Low modulus) was applied to hybrid bonding test.

4. Hybrid Bonding Demonstration

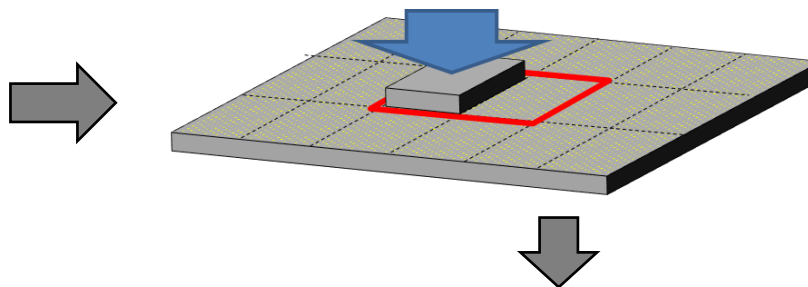
Result of HB Test

■ AFM Analysis



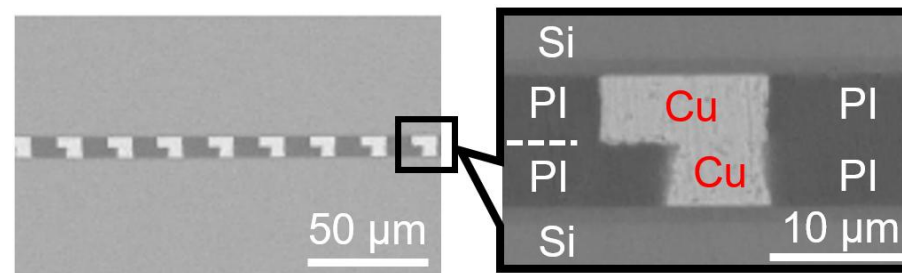
Dicing

Bonding + Annealing



Bonding Condition :
 Temp.=200°C
 Force=25N (=0.35MPa)
 Time=3s
 Annealing :
 250°C×3h

■ Cross-section SEM Image of Bonded Chips



*5nm Cu protrusion

✓ Good PI-PI and Cu-Cu joints (No void)

*Courtesy of Institute of Microelectronics,
Singapore's Agency for Science

Cu protrusion was controlled at target range of 5~15nm.
 PI-PI/Cu-Cu hybrid bonding was successfully demonstrated.

4. Hybrid Bonding Demonstration

Process Procedure for Daisy Chain Test

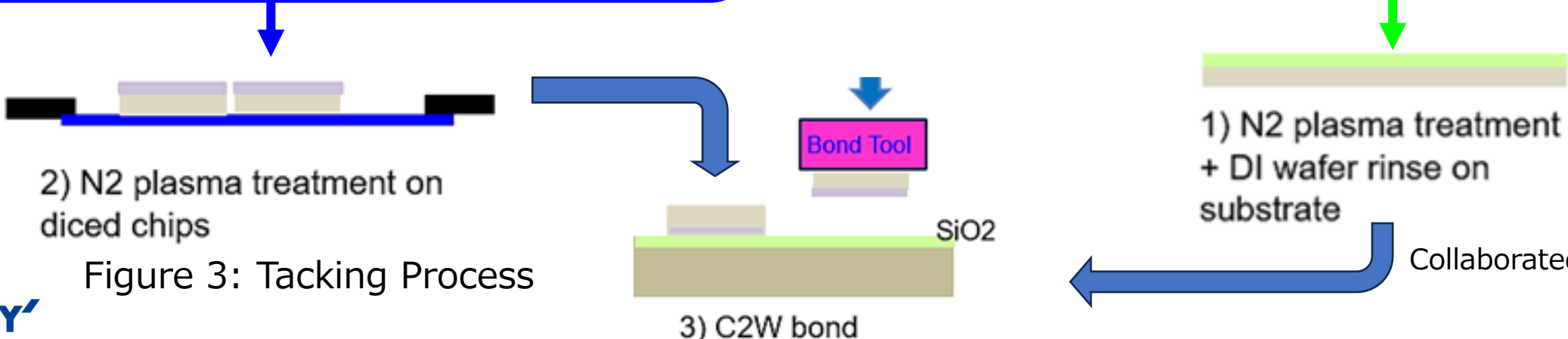
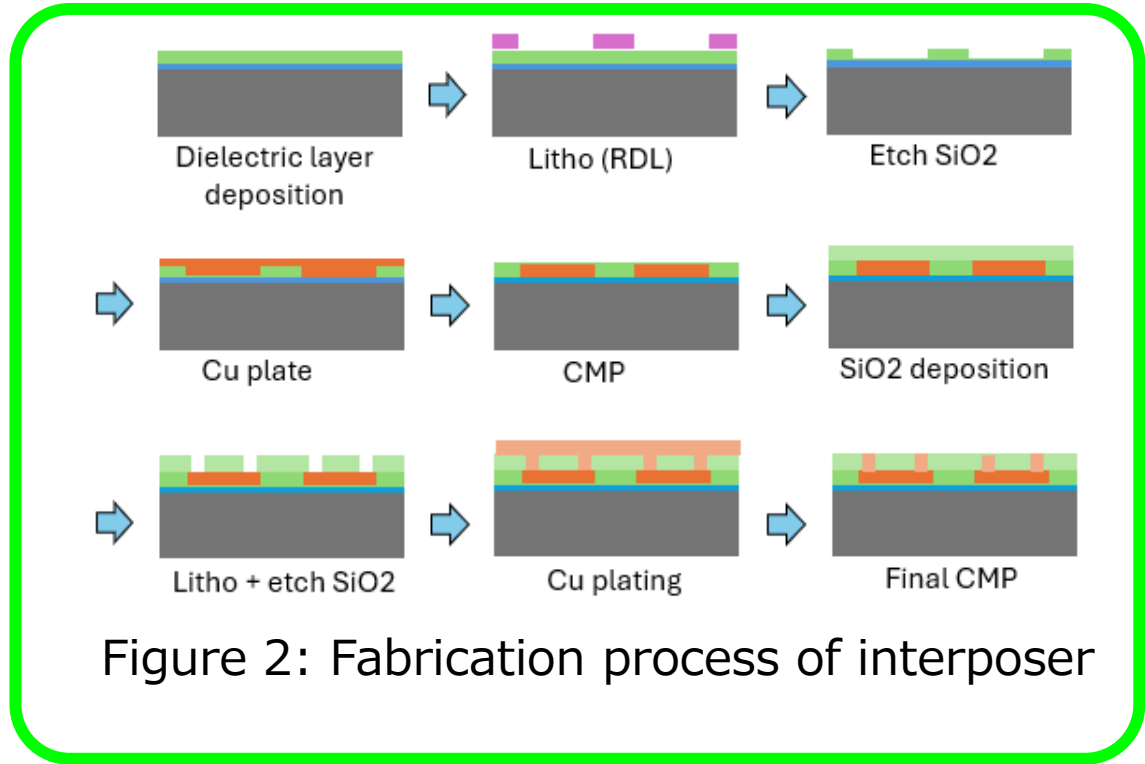
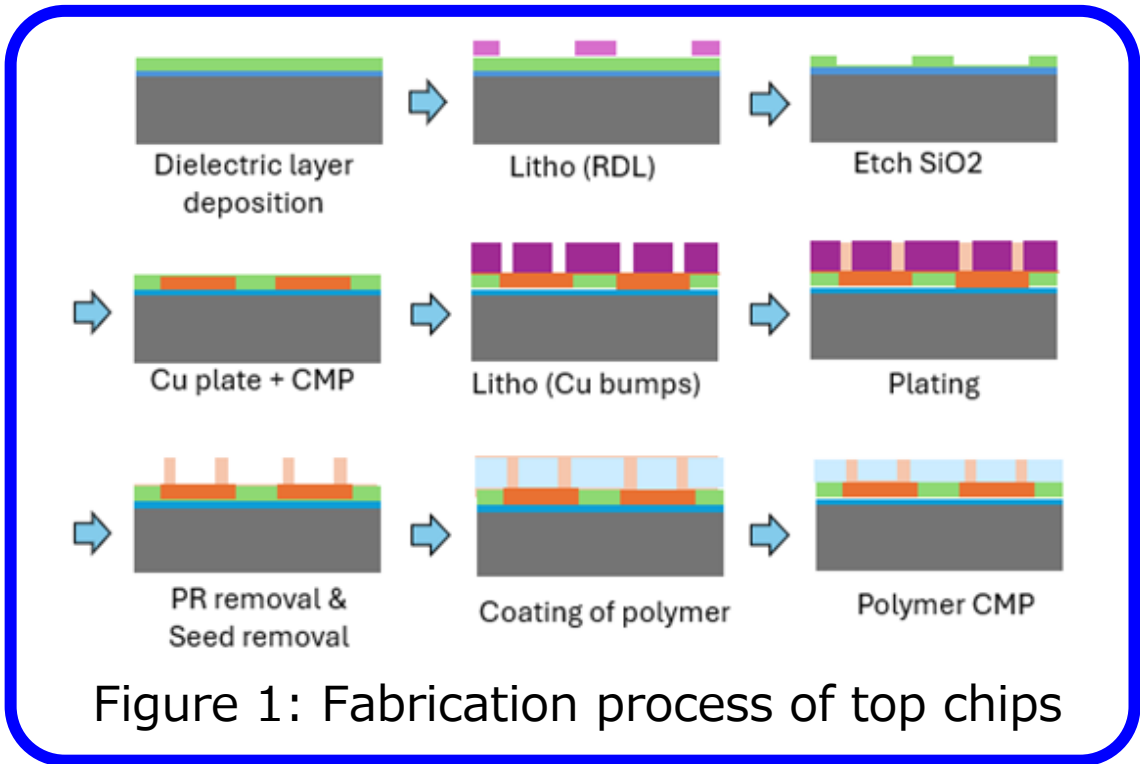


Figure 3: Tacking Process

Collaborated with IME

Summary

- 1. Low temperature curable polyimides for hybrid bonding has been developed.**
- 2. Relationship between bonding conditions and material modulus was revealed.**
- 3. PI-PI bonding mechanism was proposed by analysis of PI surface after plasma activation.**
- 4. An amount of Cu protrusion was precisely controlled by PI/Cu CMP, and hybrid bonding with low temperature curable polyimide was successfully demonstrated.**

Thank you !