



2nd Generation 3D V-Cache™ Enablement

Presenter:

Arsalan Alam

MTS, 3D Stacking Tech, Advanced Packaging

Authors: Arsalan Alam, Chandrasekhar Mandalapu, Patrick Cheng, ChihHsiang Tseng, Sean Liu, Liwei Wang, Mihaela Tanasescu, Mahmudur Chowdhury, Aly Garcia, Anadi Srivastava, Sai Vadlamani, Sireesha Gogineni, Tanmay Mudholkar, Jiali ORiain, Yash Kothari, Yong fui Lee, Litao Yang, Anna Yang, Pawan Kulkarni Aaron Blanchet, Rita Pillman, Bryan Weatherford, Tao Chen, Yolanda Liu, Raja Swaminathan

AMD 
together we advance_

Cautionary Statement

This presentation contains forward-looking statements concerning Advanced Micro Devices, Inc. (AMD) such as the features, functionality, performance, availability, timing and expected benefits of AMD products and technology as well as technology trends, innovation and roadmaps, which are made pursuant to the Safe Harbor provisions of the Private Securities Litigation Reform Act of 1995. Forward-looking statements are commonly identified by words such as "would," "may," "expects," "believes," "plans," "intends," "projects" and other terms with similar meaning. Investors are cautioned that the forward-looking statements in this presentation are based on current beliefs, assumptions and expectations, speak only as of the date of this presentation and involve risks and uncertainties that could cause actual results to differ materially from current expectations. Such statements are subject to certain known and unknown risks and uncertainties, many of which are difficult to predict and generally beyond AMD's control, that could cause actual results and other future events to differ materially from those expressed in, or implied or projected by, the forward-looking information and statements. Investors are urged to review in detail the risks and uncertainties in AMD's Securities and Exchange Commission filings, including but not limited to AMD's most recent reports on Forms 10-K and 10-Q.

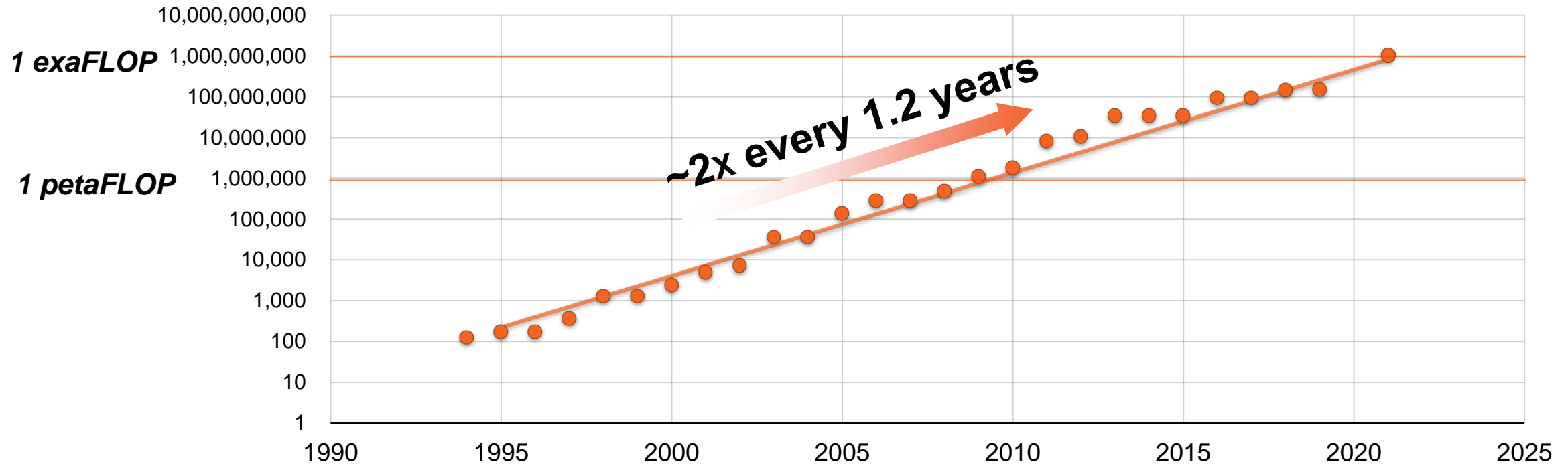
AMD does not assume, and hereby disclaims, any obligation to update forward-looking statements made in this presentation, except as may be required by law.

Outline

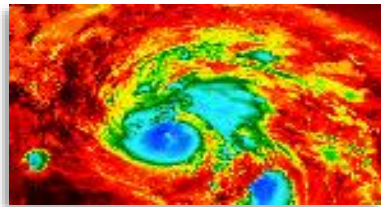
- Motivation for 3D Stacking
- Introduction to Hybrid Bonding
- 2nd Generation 3D V-Cache™ Enablement
- Reliability
- Outlook

Relentless Demand for Scientific Computing

World's Fastest Supercomputers



Space
Exploration



Climate
Change



Chemical
Sciences



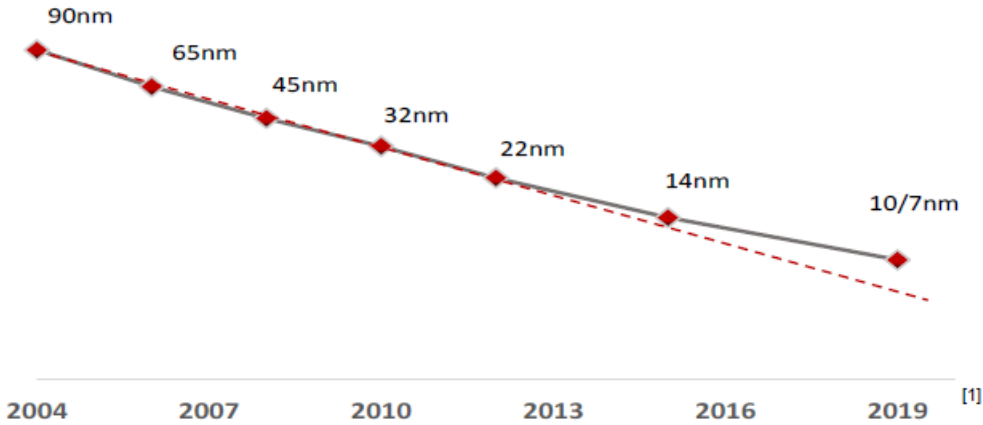
Energy
Solutions



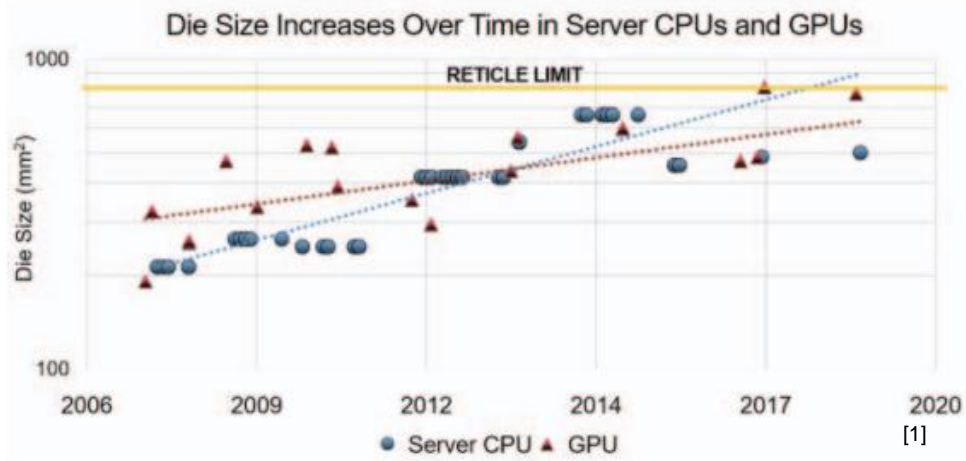
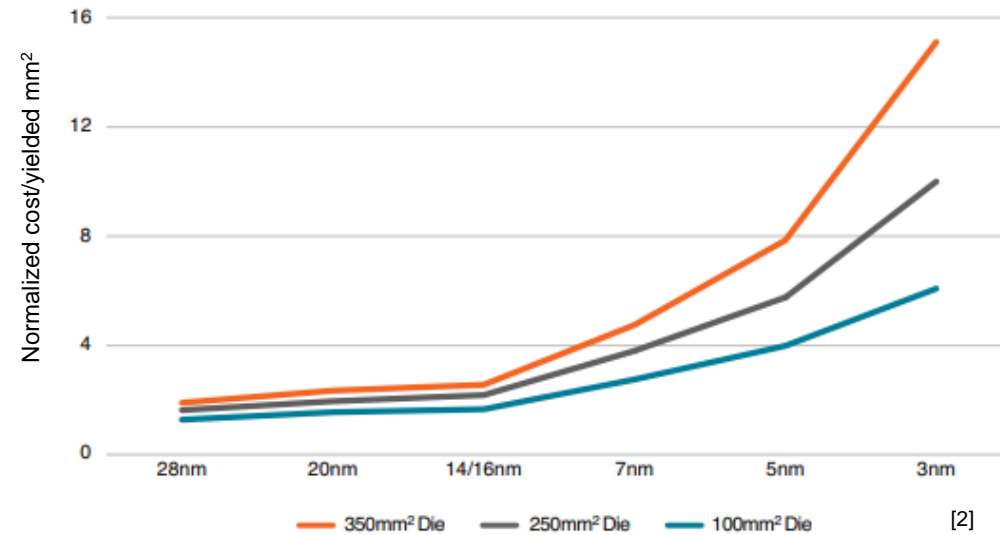
Machine
Learning

Motivation

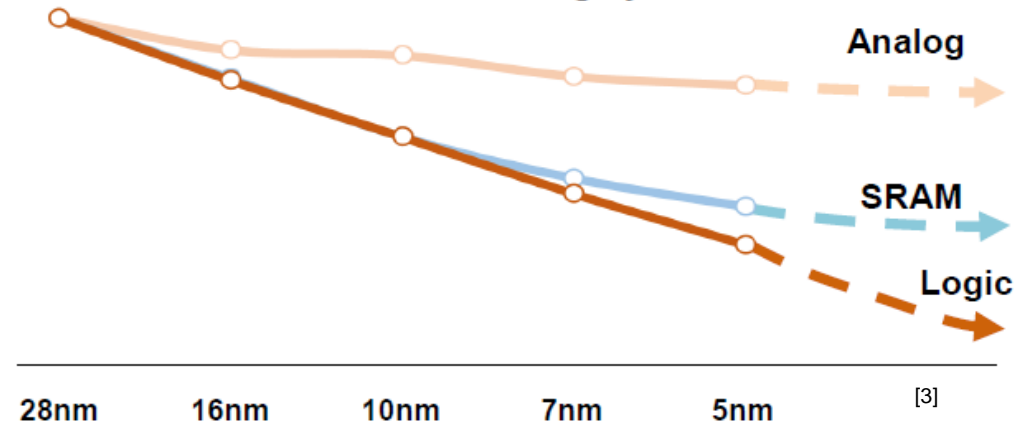
MOORE'S LAW KEEPS SLOWING



Cost Per Yielded mm² for a 350mm², 250mm², 100mm² Die

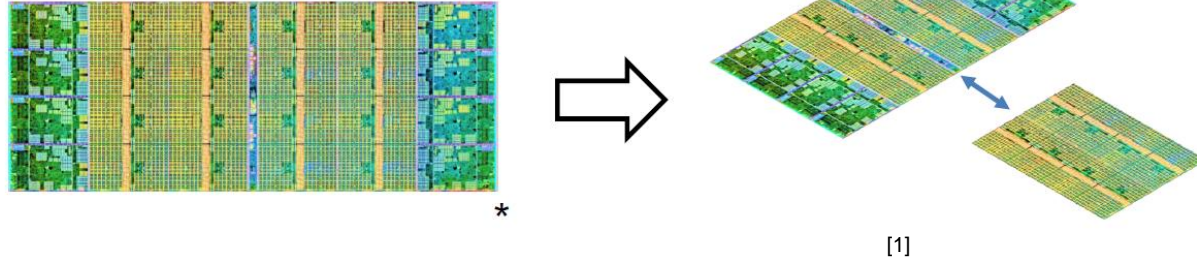


Silicon Area Scaling by Function



Motivation

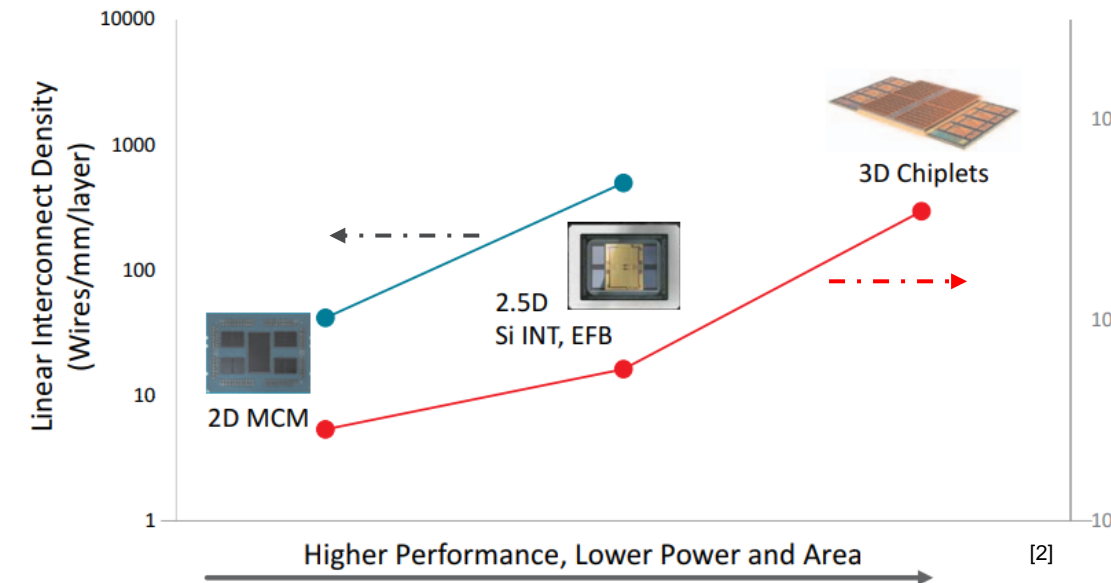
More than Moore



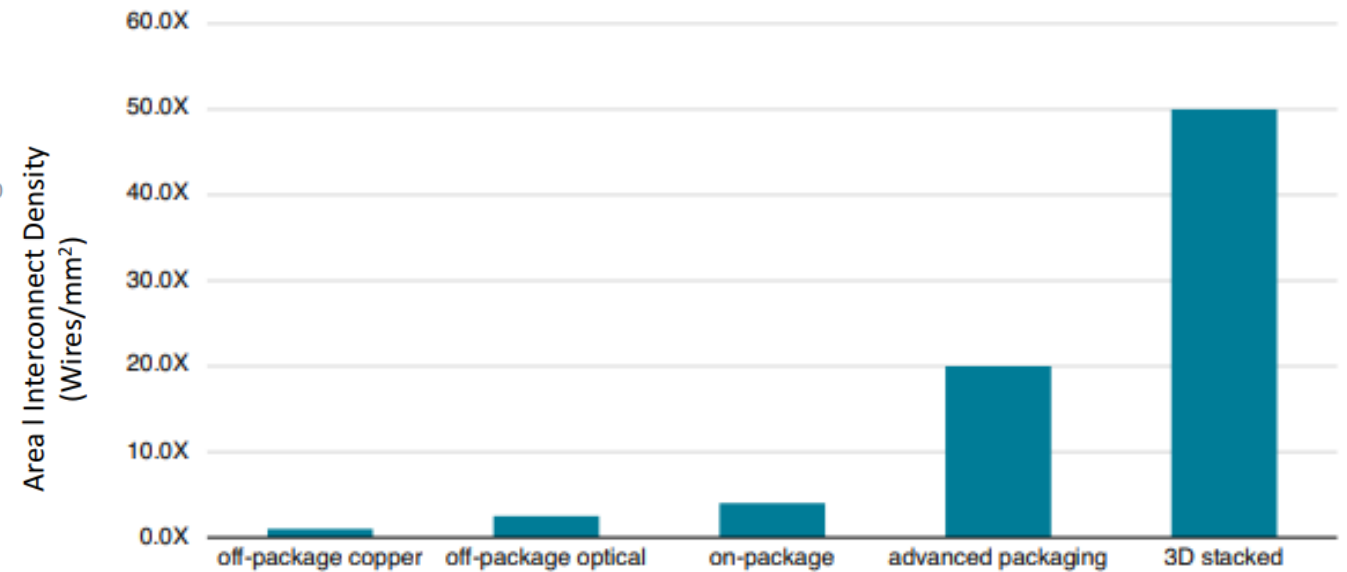
Heterogeneous Integration with 3D stacking improves system

- Performance
- Power
- Area
- Cost
- Time to market

Packaging Interconnect Density

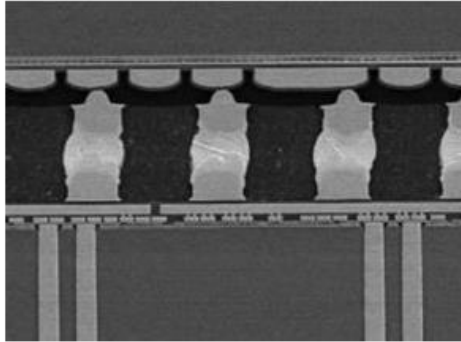


Relative Bits/Joule

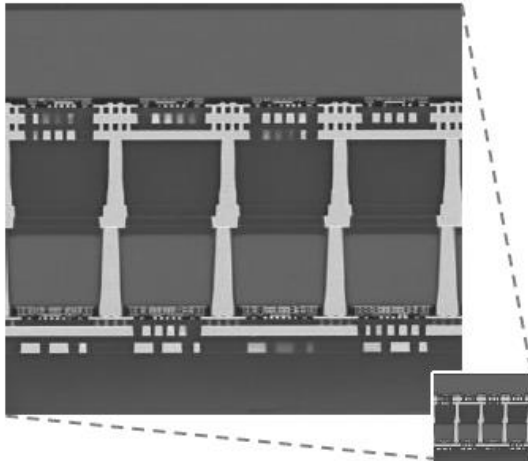


[2]

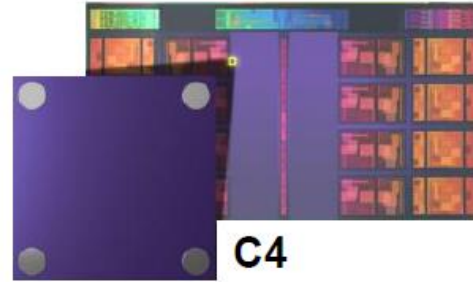
Hybrid Bonding vs. Micro Bump



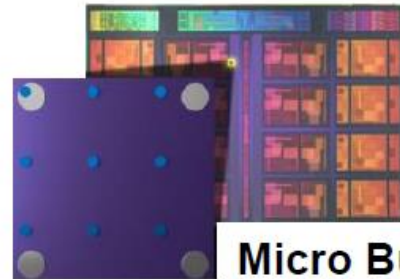
Micro Bump 3D



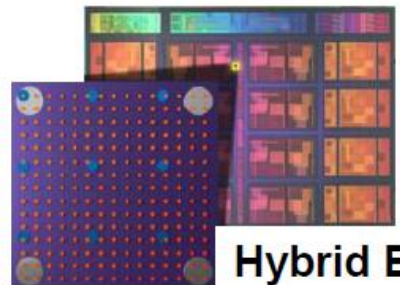
Hybrid Bond 3D



C4



Micro Bump 3D



Hybrid Bond 3D

- Compared to Micro Bump 3D solutions, Hybrid Bond offers
 - >15x interconnect density
 - >3x interconnect energy efficiency
 - Superior thermal conductance

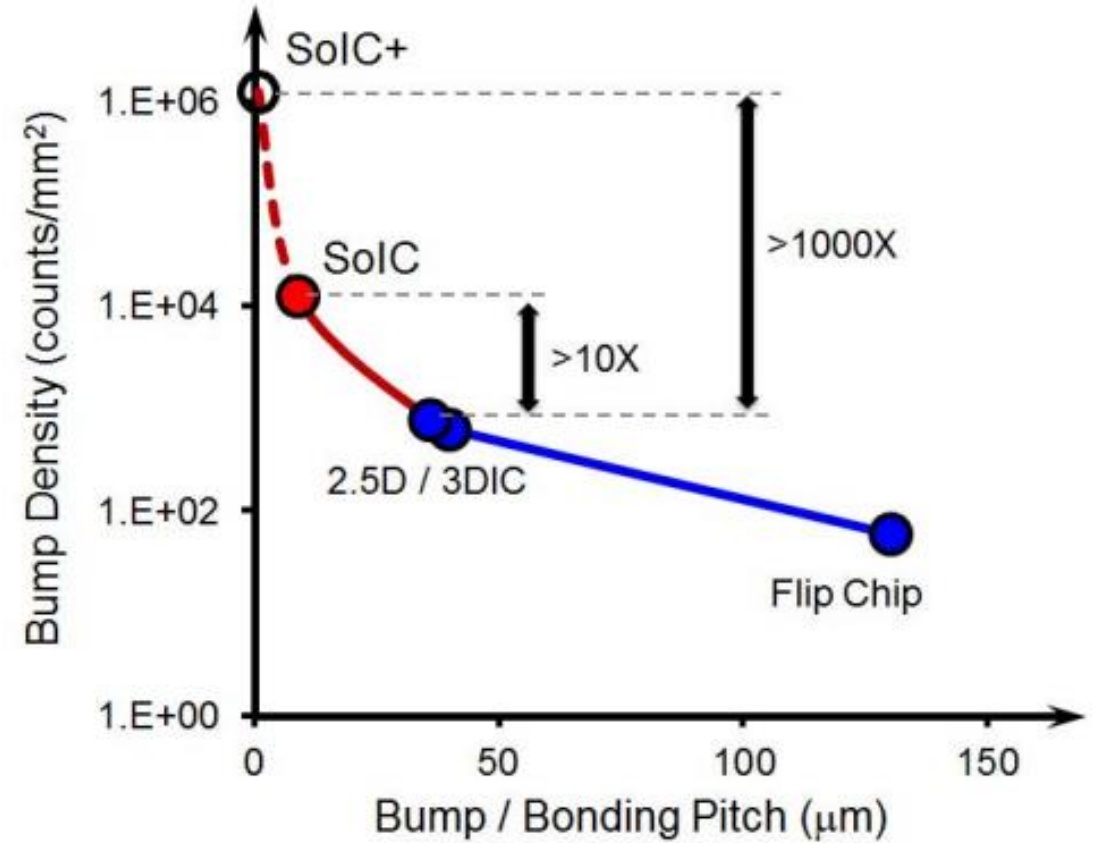
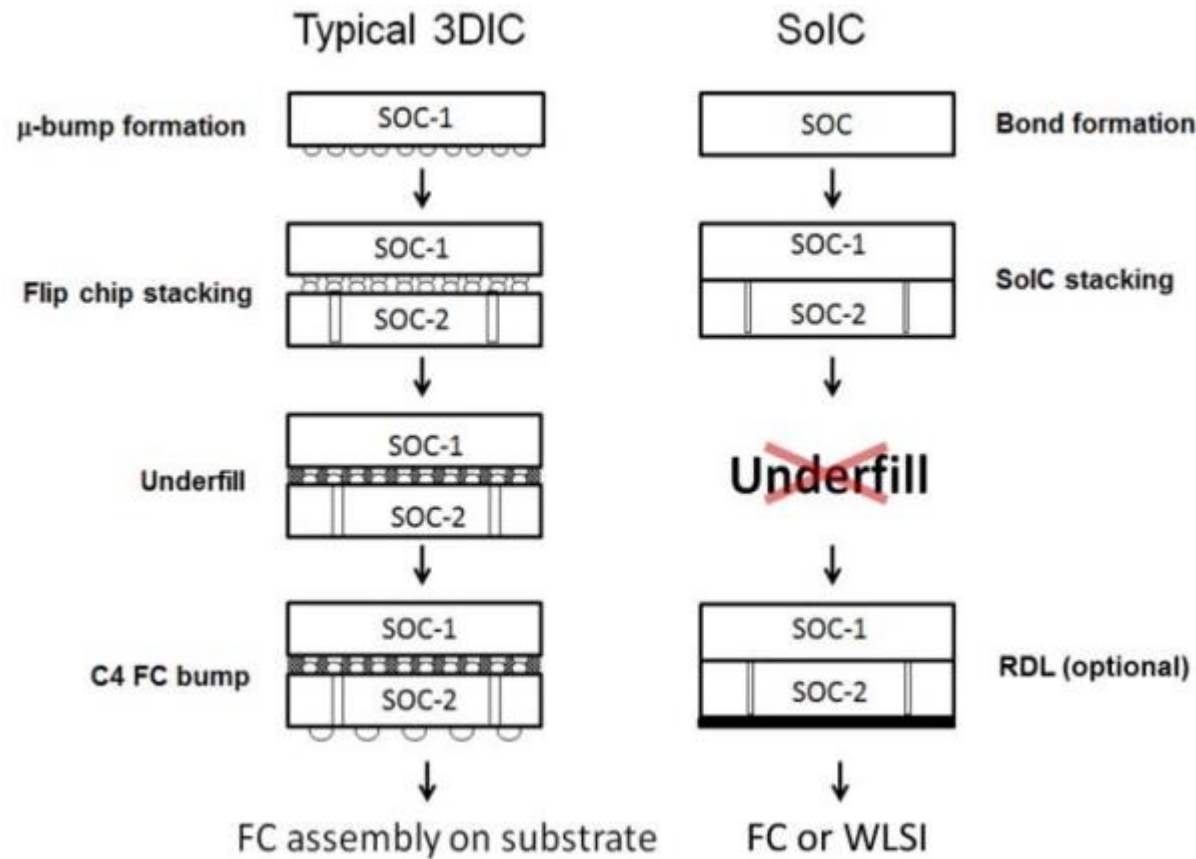
SEE ENDNOTES: EPYC-027

C4 and Micro Bump 3D illustrations are hypothetical

[1] Swaminathan, Hot Chips Tutorial, 2021

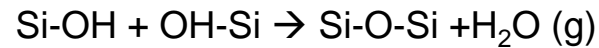
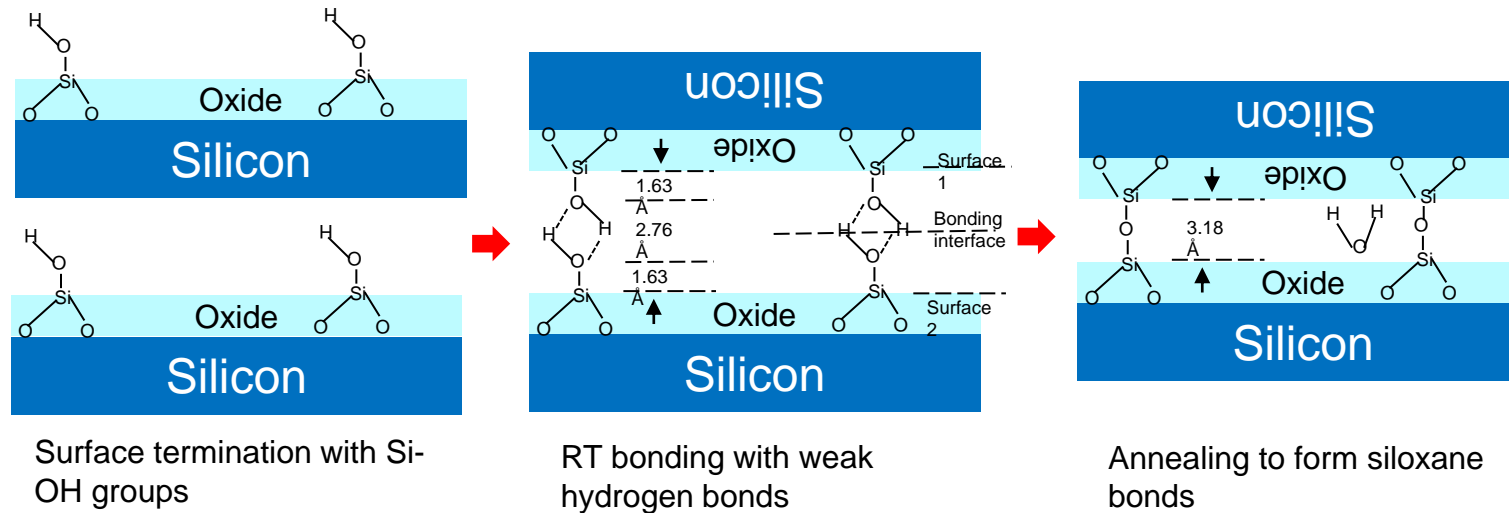
[1]

Hybrid Bonding

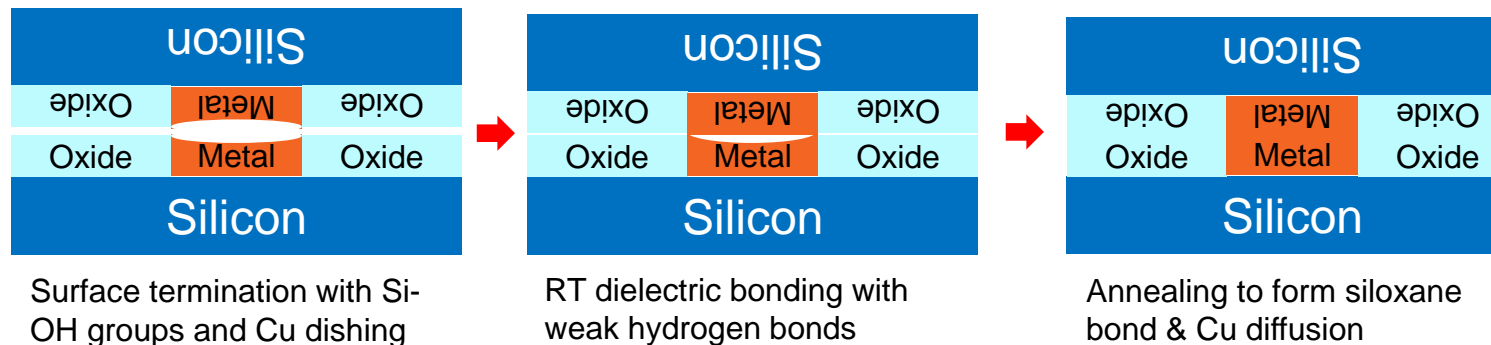


Hybrid Bonding

Fusion bonding: Dielectric materials SiO₂, SiON, SiCN



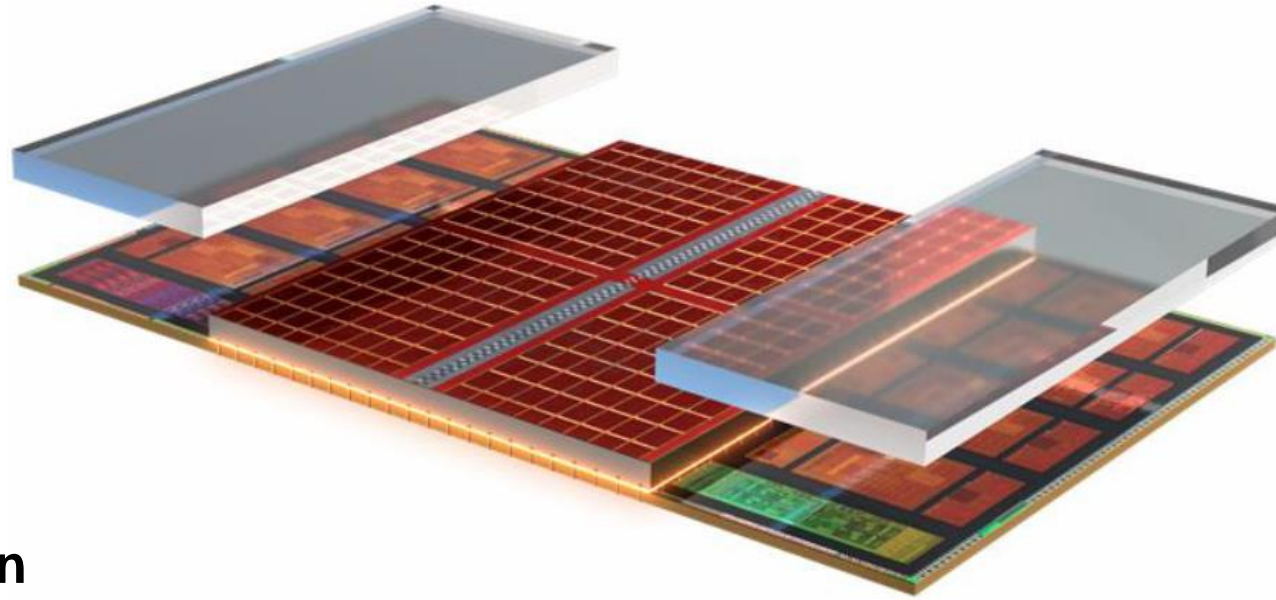
Hybrid bonding: Metal interconnects Cu, Ni



Hybrid Bonding

	D2W	WoW
Maturity		
Pitch		
Die size (TD/BD)	Need not be same size	Same/similar size
Heterogeneous Integration	Increased functionalities	Limited
Process Complexity	More complex	
Yield	KGD	Wafer dependent
Throughput		
Particle Contamination		

2nd Gen 3D V-Cache™



CCD (N5) face-down

- C4 interface to substrate
- TSV interface to L3D

L3D (N7) face-down

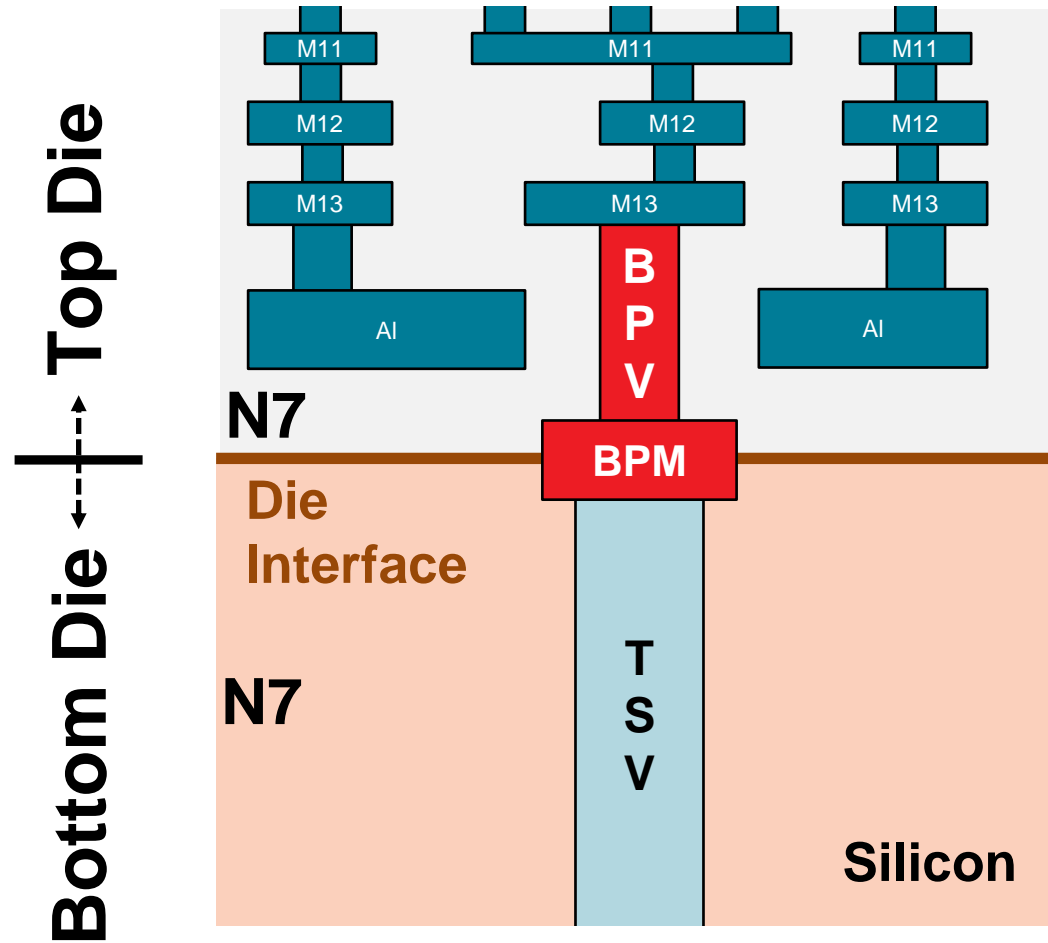
- HB to CCD
- 9 um minimum TSV/ Hybrid bond pitch

Structural Dies

- Oxide bonded to CCD

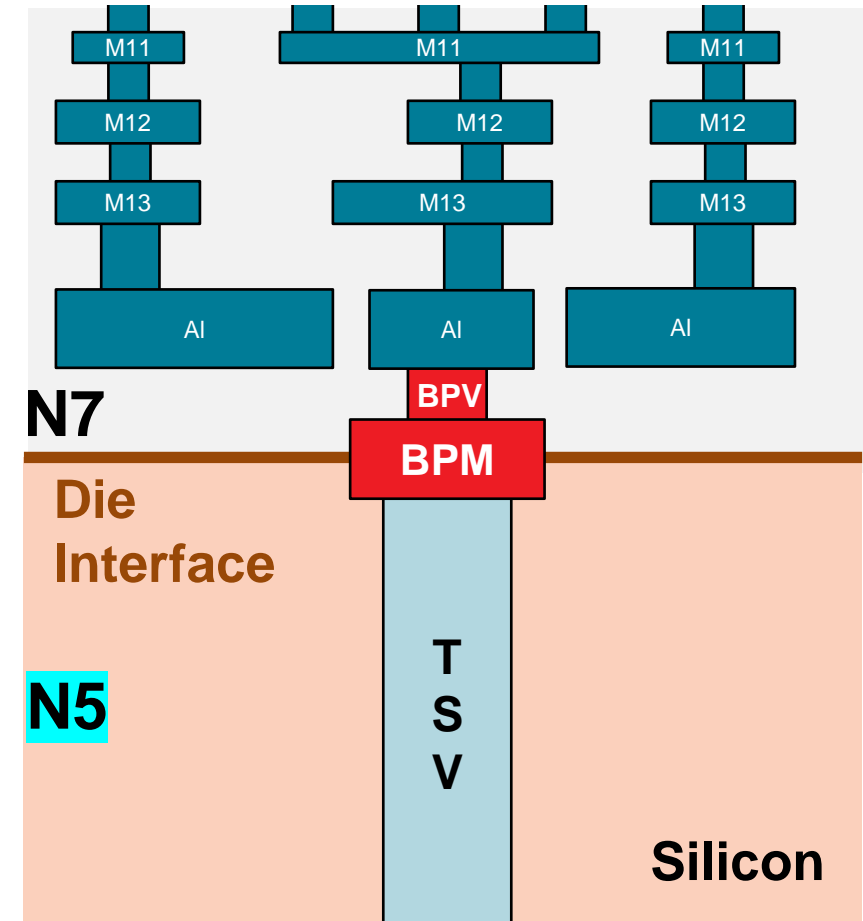
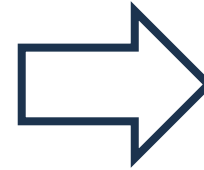
- Use of face to back (F2B) stacking of 7 nm node cache memory chiplet (SRAM chiplet) on 5 nm node logic chiplet (“Zen 4” compute chiplet)
- Bond pad via (BPV) landing on top Aluminum pad (AP) instead of top metal layer

2nd Gen 3D V-Cache™



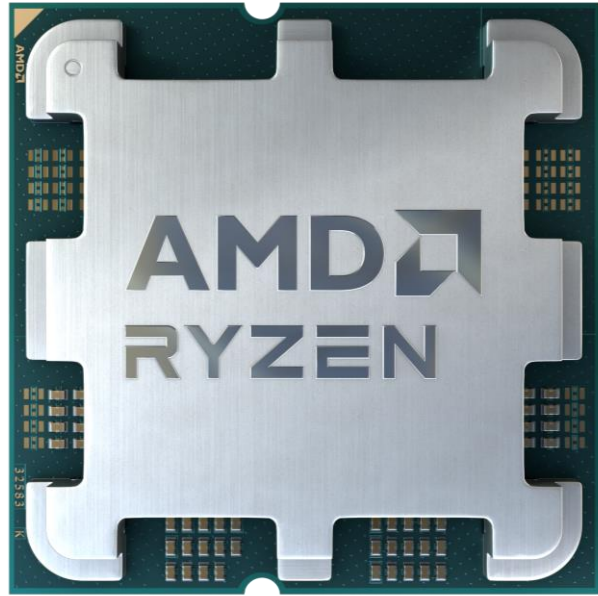
Zen3 3D V-Cache™

[1]



Zen4 3D V-Cache™

2nd Gen 3D V-Cache™



AMD RYZEN™ 7 7800X3D With AMD 3D V-Cache™ Technology

up to 8 Cores 16 Threads	up to 5.0 GHz Boost	up to 104 MB L2+L3 Cache	120W+ TDP
Processor architecture		5nm Technology	See endnotes GD-150

1920x1080 Resolution¹

Ryzen™ 7 7800X3D vs. Ryzen™ 7 5800X3D, High Image Quality Preset

AMD Ryzen™ 7 5800X3D

Cyberpunk® 2077

Shadow of the Tomb
Raider®

Borderlands® 3

F1® 22

10%

13%

22%

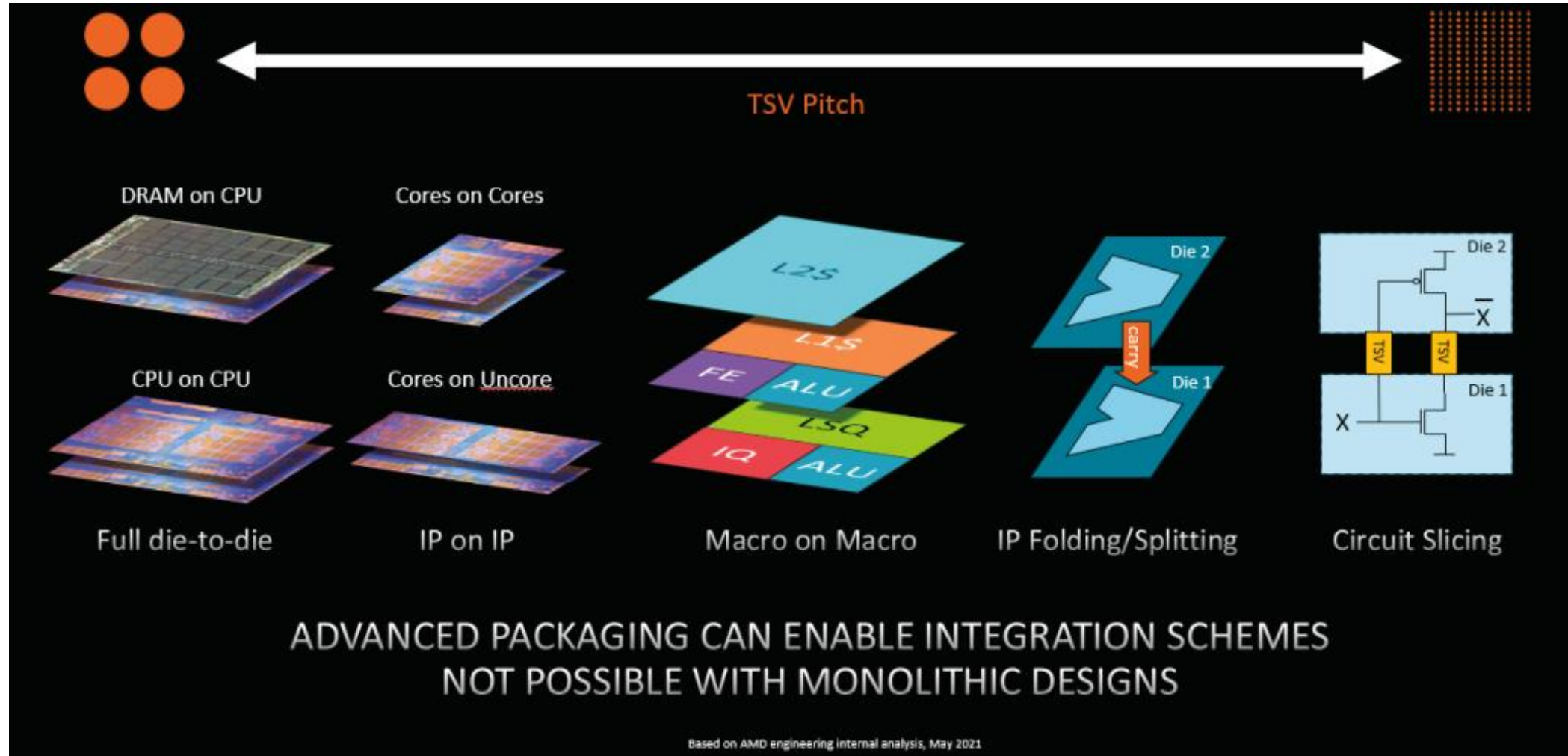
25%

Package Level Rel. Data

UHA96 hrs	Pass
TCJ2500 cyc	Pass
HTS1000 hrs	Pass

- No apparent damage to the bond interface even after extensive reliability tests

Future of 3D Stacking



Endnotes

EPYC-027: Based on AMD internal simulations and published Intel data on “Foveros” technology specifications.

Based on testing by AMD as of 12/23/2022. Testing results demonstrated in DaVinci Resolve BlackMagic , V-Ray, Blender, Cinebench R23 nT, Handbrake 1:5:1. Ryzen™ 9 7940HS system: AMD reference motherboard configured with 4x4GB LPDDR5, 1TB SSD, Radeon 780M Graphics, Windows® 11 64-bit. Apple M1 Pro system: Macbook M1 Pro 18 configured with 32GB LPDDR5, 1TB SSD, MacOS Monterey (12.6.1). System manufacturers may vary configurations, yielding different results. PHX-10.

Based on testing by AMD as of 12/23/2022. Testing results demonstrated in Handbrake, Cinebench, GeekBench, PCMark 10, Kraken, 7-Zip, Lame MP3. Ryzen™ 7 7840HS system: AMD reference motherboard configured with 4x4GB LPDDR5, 1TB SSD, Radeon 780M Graphics, Windows® 11 64-bit. Core i7-1280P system: HP Elitebook 840 G9 configured with 16GB DDR5-4800, 1TB SSD, Intel Iris Xe, Windows 11 64-bit. System manufacturers may vary configurations, yielding different results. Performance may vary. PHX-6.

Based on testing by AMD as of 12/23/2022. Testing results demonstrated in Borderlands 3, Cyberpunk 2077, Rainbow Six Siege, Assassin's Creed: Valhalla, World of Tanks Encore, League of Legends, Far Cry 6, Grand Theft Auto V, Shadow of the Tomb Raider, F1 2021, Strange Brigade, Total War: Three Kingdoms Battle. Ryzen™ 9 7940HS system: AMD reference motherboard configured with 4x4GB LPDDR5, Samsung 980 Pro 1TB SSD, Radeon 780M Graphics, Windows® 11 64-bit. Core i7-1280P system: HP Elitebook 840 G9 configured with 16GB DDR5-4800, 1TB SSD, Intel Iris Xe, Windows 11 64-bit. System manufacturers may vary configurations, yielding different results. PHX-9.

Based on testing by AMD as of 12/23/2022. Testing results demonstrated in Far Cry 6; CS:GO; Warhammer: Dawn of War 3; League of Legends. Ryzen™ 9 7945HX system: AMD reference motherboard configured with 2x16GB DDR5-5200, Samsung 980 Pro 1TB SSD, Radeon 610M Graphics, Windows® 11 64-bit. Ryzen 9 6900HX system: Alienware M17 R5 configured with 2x16GB DDR5-4800, 1TB SSD, Radeon 6850M XT graphics, Windows 11 64-bit. System manufacturers may vary configurations, yielding different results. DRG-03.

Copyright and Disclaimer

© 2024 Advanced Micro Devices, Inc. All rights reserved. AMD, the AMD Arrow logo, AMD CNDA, EPYC, AMD Instinct, Infinity Fabric, ROCm, and combinations thereof are trademarks of Advanced Micro Devices, Inc. Ubuntu and the Ubuntu logo are registered trademarks of Canonical Ltd. Red Hat, and the Red Hat logo are trademarks or registered trademarks of Red Hat, Inc. or its subsidiaries in the United States and other countries. The OpenMP name and the OpenMP logo are registered trademarks of the OpenMP Architecture Review Board. OpenCL and the OpenCL logo are trademarks of Apple Inc. used by permission by Khronos. PCI-SIG®, PCIE® and the PCI HOT PLUG design mark are registered trademarks and/or service marks of PCI-SIG. Other product names used in this publication are for identification purposes only and may be trademarks of their respective companies.

The information presented in this document is for informational purposes only and may contain technical inaccuracies, omissions, and typographical errors. The information contained herein is subject to change and may be rendered inaccurate for many reasons, including but not limited to product and roadmap changes, component and motherboard version changes, new model and/or product releases, product differences between differing manufacturers, software changes, BIOS flashes, firmware upgrades, or the like. Any computer system has risks of security vulnerabilities that cannot be completely prevented or mitigated. AMD assumes no obligation to update or otherwise correct or revise this information. However, AMD reserves the right to revise this information and to make changes from time to time to the content hereof without obligation of AMD to notify any person of such revisions or changes.

This information is provided ‘as is.’ AMD makes no representations or warranties with respect to the contents hereof and assumes no responsibility for any inaccuracies, errors, or omissions that may appear in this information. AMD specifically disclaims any implied warranties of non-infringement, merchantability, or fitness for any particular purpose. In no event will AMD be liable to any person for any reliance, direct, indirect, special, or other consequential damages arising from the use of any information contained herein, even if AMD is expressly advised of the possibility of such damages.

